



# Standard glossary of terms used in Software Testing

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**Produced by the ‘Glossary Working Party’  
International Software Testing Qualifications Board**

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## **Acknowledgements**

This document was produced by the Glossary working group of the International Software Testing Qualifications Board (ISTQB).

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## Change History

### Version 2.3 d.d. 03-28-2014

This new version has been developed to support the Foundation Extension Agile Tester syllabus. In addition a number of change request have been implemented in version 2.3 of the ISTQB Glossary.

#### New terms added:

- build verification test
- burndown chart
- BVT
- content reference model
- escaped defect
- MCDC
- n-wise testing
- PRISMA
- process reference model
- Product RiSk MAnagement
- QFD
- quality function deployment
- system under test
- TDD
- test analysis
- test reporting
- traceability matrix

#### Terms changed;

- acceptance criteria
- accuracy testing
- agile manifesto
- agile software development
- agile testing
- classification tree method
- combinatorial testing
- configuration item
- configuration management
- confirmation testing
- defect-based test design technique
- Defect Detection Percentage (DDP)
- defect taxonomy
- exploratory testing
- factory acceptance testing
- incremental development model
- iterative development model
- maintainability testing
- model-based testing
- orthogonal array testing
- pairwise testing
- performance testing
- product risk
- quality risk
- re-testing
- regression testing
- risk analysis
- risk assessment
- security testing
- smoke test
- software lifecycle
- test approach
- test automation
- test basis
- test charter
- test design
- test-driven development
- test estimation
- test execution automation
- test oracle
- test strategy
- unit test framework

	<ul style="list-style-type: none"><li>- usability testing</li><li>- user story</li></ul>
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## Foreword

In compiling this glossary the working party has sought the views and comments of as broad a spectrum of opinion as possible in industry, commerce and government bodies and organizations, with the aim of producing an international testing standard which would gain acceptance in as wide a field as possible. Total agreement will rarely, if ever, be achieved in compiling a document of this nature. Contributions to this glossary have been received from the testing communities from all over the world.

Many software testers have used BS 7925-1, the British Standard *Glossary of Software Testing Terms*, since its original publication in 1998. The standard was initially developed with a bias towards component testing, but, since its publication, many comments and proposals for new definitions have been submitted to both improve and expand the standard to cover a wider range of software testing. The ISTQB testing glossary has incorporated many of these suggested updates. It is used as a reference document for the International Software Testing Qualification Board (ISTQB) software testing qualification scheme.

The ISTQB Glossary has two main objectives:

- Support the understanding of ISTQB syllabi by defining the terms used in the various syllabi
- Support communication within the international testing community and with its stakeholders by providing a standard testing vocabulary.

ISTQB National or Regional Boards can use the ISTQB Glossary, to translate into their local language. These boards may adapt the ISTQB Glossary to their particular language needs.

## 1. Introduction

Much time and effort is wasted both within and between industry, commerce, government and professional and academic institutions when ambiguities arise as a result of the inability to differentiate adequately between such terms as ‘statement coverage’ and ‘decision coverage’; ‘test suite’, ‘test specification’ and ‘test plan’ and similar terms which form an interface between various sectors of society. Moreover, the professional or technical use of these terms is often at variance, with different meanings attributed to them.

## 2. Scope

This document presents concepts, terms and definitions designed to aid communication in (software) testing and related disciplines. It supports the terminology used in the various ISTQB syllabi.

However, common agile terminology used in the Foundation Agile extension syllabus is not covered by this glossary. For these terms the Foundation Extension Agile Tester syllabus identifies a number of well-accepted internet resources which provide definitions.

## 3. Glossary structure

### Arrangement

The glossary has been arranged in a single section of definitions ordered alphabetically. Some terms are preferred to other synonymous ones, in which case, the definition of the preferred term appears, with the synonymous ones referring to that. For example *structural testing* refers to *white box testing*. For synonyms, the “See” indicator is used

“See also” cross-references are also used. They assist the user to quickly navigate to the right index term. “See also” cross-references are constructed for relationships such as broader term to a narrower term, and overlapping meaning between two terms.

### **Keywords**

The ISTQB Glossary contains many terms for different reasons. Some are provided to “just” support the reader of an ISTQB syllabus in understanding the text. Some are there because the term was used in a previous version of a syllabus and the principle of backwards compatibility is being applied. However, probably the most important terms are the (examinable) keywords that are explicitly identified by the various ISTQB syllabi. An important user group of these keywords are the (test)professional who are preparing for an ISTQB exam. To support them, the keywords that they need to understand for a particular exam are indicated in this glossary. Note that the principle of inheritance is applicable, e.g., at an ISTQB Advanced exam one still needs to understand all ISTQB Foundation keywords. The keywords are indicated in the following manner:

F : Keyword ISTQB Foundation syllabus  
F-AT : Keyword ISTQB Foundation Extension Agile Tester syllabus  
ATM : Keyword ISTQB Advanced – Test Management syllabus  
ATA : Keyword ISTQB Advanced – Test Analyst syllabus  
ATT : Keyword ISTQB Advanced – Technical Test Analyst syllabus  
EITP : Keyword ISTQB Expert – Improving the Testing Process syllabus  
ETM : Keyword ISTQB Expert – Test Management syllabus.

Note that if a keyword is identified in a syllabus, but is not the preferred term according to the glossary both the keyword and the term it refers to (using the “See” indicator) are labeled with the appropriate syllabus indicator.

### **References**

In this glossary references, are used in two ways.

- Square brackets without the addition of “after”, e.g., [ISO 9126], mean that the exact text of the reference is used.
- In case a definition from a reference has been adapted to the context of the ISTQB Glossary by minor changes, the addition “after” is used, e.g., [After ISO 9126].

## **4. Trademarks**

In this document the following trademarks are used:

- CMMI and IDEAL are registered trademarks of Carnegie Mellon University
- EFQM is a registered trademark of the EFQM Foundation
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- TMMi is a registered trademark of the TMMi Foundation

## 5. Definitions

### A

**abstract test case:** See *high level test case*.

**acceptance:** See *acceptance testing*.

F-AT **acceptance criteria:** The exit criteria that a component or system must satisfy in order to be accepted by a user, customer, or other authorized entity. [IEEE 610]

**acceptance testing:** Formal testing with respect to user needs, requirements, and business processes conducted to determine whether or not a system satisfies the acceptance criteria and to enable the user, customers or other authorized entity to determine whether or not to accept the system. [After IEEE 610]

ATA **accessibility testing:** Testing to determine the ease by which users with disabilities can use a component or system. [Gerrard]

**accuracy:** The capability of the software product to provide the right or agreed results or effects with the needed degree of precision. [ISO 9126] See also *functionality*.

ATA **accuracy testing:** The process of testing to determine the accuracy of a software product. See also *accuracy*.

EITP **acting (IDEAL):** The phase within the IDEAL model where the improvements are developed, put into practice, and deployed across the organization. The acting phase consists of the activities: create solution, pilot/test solution, refine solution and implement solution. See also *IDEAL*.

**action word driven testing:** See *keyword-driven testing*

**actor:** User or any other person or system that interacts with the system under test in a specific way.

**actual outcome:** See *actual result*.

**actual result:** The behavior produced/observed when a component or system is tested.

**ad hoc review:** See *informal review*.

**ad hoc testing:** Testing carried out informally; no formal test preparation takes place, no recognized test design technique is used, there are no expectations for results and arbitrariness guides the test execution activity.

ATT **adaptability:** The capability of the software product to be adapted for different specified environments without applying actions or means other than those provided for this purpose for the software considered. [ISO 9126] See also *portability*.

F-AT **agile manifesto:** A statement on the values that underpin agile software development. The values are:

- individuals and interactions over processes and tools
- working software over comprehensive documentation
- customer collaboration over contract negotiation
- responding to change over following a plan.

F-AT **agile software development:** A group of software development methodologies based on  
EITP iterative incremental development, where requirements and solutions evolve through collaboration between self-organizing cross-functional teams.



- EITP **agile testing:** Testing practice for a project using agile software development methodologies, incorporating techniques and methods, such as extreme programming (XP), treating development as the customer of testing and emphasizing the test-first design paradigm. See also *test-driven development*.
- algorithm test:** [TMap] See *branch testing*.
- F **alpha testing:** Simulated or actual operational testing by potential users/customers or an independent test team at the developers' site, but outside the development organization. Alpha testing is often employed for off-the-shelf software as a form of internal acceptance testing.
- ETM **analytical testing:** Testing based on a systematic analysis of e.g., product risks or requirements.
- ATT **analyzability:** The capability of the software product to be diagnosed for deficiencies or causes of failures in the software, or for the parts to be modified to be identified. [ISO 9126] See also *maintainability*.
- analyzer:** See *static analyzer*.
- ATM **anomaly:** Any condition that deviates from expectation based on requirements specifications, design documents, user documents, standards, etc. or from someone's perception or experience. Anomalies may be found during, but not limited to, reviewing, testing, analysis, compilation, or use of software products or applicable documentation. [IEEE 1044] See also *bug, defect, deviation, error, fault, failure, incident, problem*.
- ATT **anti-pattern:** Repeated action, process, structure or reusable solution that initially appears to be beneficial and is commonly used but is ineffective and/or counterproductive in practice.
- API (Application Programming Interface) testing:** Testing the code which enables communication between different processes, programs and/or systems. API testing often involves negative testing, e.g., to validate the robustness of error handling. See also *interface testing*.
- arc testing:** See *branch testing*.
- EITP **assessment report:** A document summarizing the assessment results, e.g. conclusions, recommendations and findings. See also *process assessment*.
- EITP **assessor:** A person who conducts an assessment; any member of an assessment team.
- ATT **atomic condition:** A condition that cannot be decomposed, i.e., a condition that does not contain two or more single conditions joined by a logical operator (AND, OR, XOR).
- F **attack:** Directed and focused attempt to evaluate the quality, especially reliability, of a test object by attempting to force specific failures to occur. See also *negative testing*.
- attack-based testing:** An experience-based testing technique that uses software attacks to induce failures, particularly security related failures. See also *attack*.
- ATA **attractiveness:** The capability of the software product to be attractive to the user. [ISO 9126] See also *usability*.
- ATM **audit:** An independent evaluation of software products or processes to ascertain compliance to standards, guidelines, specifications, and/or procedures based on objective criteria, including documents that specify:
- (1) the form or content of the products to be produced

- (2) the process by which the products shall be produced
- (3) how compliance to standards or guidelines shall be measured. [IEEE 1028]

**audit trail:** A path by which the original input to a process (e.g. data) can be traced back through the process, taking the process output as a starting point. This facilitates defect analysis and allows a process audit to be carried out. [After TMap]

**automated testware:** Testware used in automated testing, such as tool scripts.

**availability:** The degree to which a component or system is operational and accessible when required for use. Often expressed as a percentage. [IEEE 610]

## B

**back-to-back testing:** Testing in which two or more variants of a component or system are executed with the same inputs, the outputs compared, and analyzed in cases of discrepancies. [IEEE 610]

EITP **balanced scorecard:** A strategic tool for measuring whether the operational activities of a company are aligned with its objectives in terms of business vision and strategy. See also *corporate dashboard, scorecard*.

**baseline:** A specification or software product that has been formally reviewed or agreed upon, that thereafter serves as the basis for further development, and that can be changed only through a formal change control process. [After IEEE 610]

**basic block:** A sequence of one or more consecutive executable statements containing no branches. Note: A node in a control flow graph represents a basic block.

**basis test set:** A set of test cases derived from the internal structure of a component or specification to ensure that 100% of a specified coverage criterion will be achieved.

**debugging:** [Abbott] See *fault seeding*.

**behavior:** The response of a component or system to a set of input values and preconditions.

**benchmark test:** (1) A standard against which measurements or comparisons can be made.  
(2) A test that is be used to compare components or systems to each other or to a standard as in (1). [After IEEE 610]

**bespoke software:** Software developed specifically for a set of users or customers. The opposite is off-the-shelf software.

**best practice:** A superior method or innovative practice that contributes to the improved performance of an organization under given context, usually recognized as ‘best’ by other peer organizations.

F **beta testing:** Operational testing by potential and/or existing users/customers at an external site not otherwise involved with the developers, to determine whether or not a component or system satisfies the user/customer needs and fits within the business processes. Beta testing is often employed as a form of external acceptance testing for off-the-shelf software in order to acquire feedback from the market.

**big-bang testing:** An integration testing approach in which software elements, hardware elements, or both are combined all at once into a component or an overall system, rather than in stages. [After IEEE 610] See also *integration testing*.

**black box technique:** See *black box test design technique*.

F **black box test design technique:** Procedure to derive and/or select test cases based on an

- ATA analysis of the specification, either functional or non-functional, of a component or system without reference to its internal structure.
- F **black box testing:** Testing, either functional or non-functional, without reference to the internal structure of the component or system.
- blocked test case:** A test case that cannot be executed because the preconditions for its execution are not fulfilled.
- bottom-up testing:** An incremental approach to integration testing where the lowest level components are tested first, and then used to facilitate the testing of higher level components. This process is repeated until the component at the top of the hierarchy is tested. See also *integration testing*.
- boundary value:** An input value or output value which is on the edge of an equivalence partition or at the smallest incremental distance on either side of an edge, for example the minimum or maximum value of a range.
- F **boundary value analysis:** A black box test design technique in which test cases are designed  
ATA based on boundary values. See also *boundary value*.
- boundary value coverage:** The percentage of boundary values that have been exercised by a test suite.
- boundary value testing:** See *boundary value analysis*.
- branch:** A basic block that can be selected for execution based on a program construct in which one of two or more alternative program paths is available, e.g. case, jump, go to, if-then-else.
- branch condition:** See *condition*.
- branch condition combination coverage:** See *multiple condition coverage*.
- branch condition combination testing:** See *multiple condition testing*.
- branch condition coverage:** See *condition coverage*.
- branch coverage:** The percentage of branches that have been exercised by a test suite. 100% branch coverage implies both 100% decision coverage and 100% statement coverage.
- branch testing:** A white box test design technique in which test cases are designed to execute branches.
- buffer:** A device or storage area used to store data temporarily for differences in rates of data flow, time or occurrence of events, or amounts of data that can be handled by the devices or processes involved in the transfer or use of the data. [IEEE 610]
- buffer overflow:** A memory access failure due to the attempt by a process to store data beyond the boundaries of a fixed length buffer, resulting in overwriting of adjacent memory areas or the raising of an overflow exception. See also *buffer*.
- F **bug:** See *defect*.
- bug report:** See *defect report*.
- bug taxonomy:** See *defect taxonomy*.
- bug tracking tool:** See *defect management tool*.
- F-AT **build verification test:** A set of automated tests which validates the integrity of each new

build and verifies its key/core functionality, stability and testability. It is an industry practice when a high frequency of build releases occurs (e.g., agile projects) and it is run on every new build before the build is released for further testing. See also *regression testing*, *smoke test*.

**burndown chart:** A publicly displayed chart that depicts the outstanding effort versus time in an iteration. It shows the status and trend of completing the tasks of the iteration. The X-axis typically represents days in the sprint, while the Y-axis is the remaining effort (usually either in ideal engineering hours or story points).

**business process-based testing:** An approach to testing in which test cases are designed based on descriptions and/or knowledge of business processes.

**BVT:** See *build verification test*.

## C

**call graph:** An abstract representation of calling relationships between subroutines in a program.

ATM **Capability Maturity Model Integration:** A framework that describes the key  
EITP elements of an effective product development and maintenance process. The Capability Maturity Model Integration covers best-practices for planning, engineering and managing product development and maintenance. [CMMI]

ATT **capture/playback tool:** A type of test execution tool where inputs are recorded during manual testing in order to generate automated test scripts that can be executed later (i.e. replayed). These tools are often used to support automated regression testing.

**capture/replay tool:** See *capture/playback tool*.

**CASE:** Acronym for Computer Aided Software Engineering.

**CAST:** Acronym for Computer Aided Software Testing. See also *test automation*.

EITP **causal analysis:** The analysis of defects to determine their root cause. [CMMI]

**cause-effect analysis:** See *cause-effect graphing*.

**cause-effect decision table:** See *decision table*.

EITP **cause-effect diagram:** A graphical representation used to organize and display the interrelationships of various possible root causes of a problem. Possible causes of a real or potential defect or failure are organized in categories and subcategories in a horizontal tree-structure, with the (potential) defect or failure as the root node. [After Juran]

EITP **cause-effect graph:** A graphical representation of inputs and/or stimuli (causes) with their associated outputs (effects), which can be used to design test cases.

ATA **cause-effect graphing:** A black box test design technique in which test cases are designed from cause-effect graphs. [BS 7925/2]

**certification:** The process of confirming that a component, system or person complies with its specified requirements, e.g. by passing an exam.

**change control:** See *configuration control*.

**change control board:** See *configuration control board*.

- EITP **change management:** (1) A structured approach to transitioning individuals, and organizations from a current state to a desired future state. (2) Controlled way to effect a change, or a proposed change, to a product or service. See also *configuration management*.
- ATT **changeability:** The capability of the software product to enable specified modifications to be implemented. [ISO 9126] See also *maintainability*.
- charter:** See *test charter*.
- checker:** See *reviewer*.
- ATA **checklist-based testing:** An experience-based test design technique whereby the experienced tester uses a high-level list of items to be noted, checked, or remembered, or a set of rules or criteria against which a product has to be verified.
- Chow's coverage metrics:** See *N-switch coverage*. [Chow]
- classification tree:** A tree showing equivalence partitions hierarchically ordered, which is used to design test cases in the classification tree method. See also *classification tree method*.
- ATA **classification tree method:** A black box test design technique in which test cases, described by means of a classification tree, are designed to execute combinations of representatives of input and/or output domains. [Grochtmann] See also *combinatorial testing*.
- clear-box testing:** See *white-box testing*.
- ATM **CMMI:** See *Capability Maturity Model Integration*.
- EITP **code:** Computer instructions and data definitions expressed in a programming language or in a form output by an assembler, compiler or other translator. [IEEE 610]
- code analyzer:** See *static code analyzer*.
- F **code coverage:** An analysis method that determines which parts of the software have been executed (covered) by the test suite and which parts have not been executed, e.g. statement coverage, decision coverage or condition coverage.
- code-based testing:** See *white box testing*.
- EITP **codependent behavior:** Excessive emotional or psychological dependence on another person, specifically in trying to change that person's current (undesirable) behavior while supporting them in continuing that behavior. For example, in software testing, complaining about late delivery to test and yet enjoying the necessary "heroism" working additional hours to make up time when delivery is running late, therefore reinforcing the lateness.
- ATT **co-existence:** The capability of the software product to co-exist with other independent software in a common environment sharing common resources. [ISO 9126] See also *portability*.
- ATA **combinatorial testing:** A means to identify a suitable subset of test combinations to achieve a predetermined level of coverage when testing an object with multiple parameters and where those parameters themselves each have several values, which gives rise to more combinations than are feasible to test in the time allowed. See also *classification tree method*, *n-wise testing*, *pairwise testing*, *orthogonal array testing*.
- F **Commercial Off-The-Shelf software:** See *off-the-shelf software*.
- comparator:** See *test comparator*.

- compatibility testing:** See *interoperability testing*.
- F **compiler:** A software tool that translates programs expressed in a high order language into their machine language equivalents. [IEEE 610]
- complete testing:** See *exhaustive testing*.
- completion criteria:** See *exit criteria*.
- F **complexity:** The degree to which a component or system has a design and/or internal structure that is difficult to understand, maintain and verify. See also *cyclomatic complexity*.
- compliance:** The capability of the software product to adhere to standards, conventions or regulations in laws and similar prescriptions. [ISO 9126]
- compliance testing:** The process of testing to determine the compliance of the component or system.
- component:** A minimal software item that can be tested in isolation.
- component integration testing:** Testing performed to expose defects in the interfaces and interaction between integrated components.
- component specification:** A description of a component's function in terms of its output values for specified input values under specified conditions, and required non-functional behavior (e.g. resource-utilization).
- F **component testing:** The testing of individual software components. [After IEEE 610]
- compound condition:** Two or more single conditions joined by means of a logical operator (AND, OR or XOR), e.g. 'A>B AND C>1000'.
- ATA **concrete test case:** See *low level test case*.
- concurrency testing:** Testing to determine how the occurrence of two or more activities within the same interval of time, achieved either by interleaving the activities or by simultaneous execution, is handled by the component or system. [After IEEE 610]
- condition:** A logical expression that can be evaluated as True or False, e.g. A>B. See also *condition testing*.
- condition combination coverage:** See *multiple condition coverage*.
- condition combination testing:** See *multiple condition testing*.
- condition coverage:** The percentage of condition outcomes that have been exercised by a test suite. 100% condition coverage requires each single condition in every decision statement to be tested as True and False.
- condition determination coverage:** See *modified condition decision coverage*.
- condition determination testing:** See *modified condition decision testing*.
- condition outcome:** The evaluation of a condition to True or False.
- ATT **condition testing:** A white box test design technique in which test cases are designed to execute condition outcomes.
- ETM **confidence interval:** In managing project risks, the period of time within which a contingency action must be implemented in order to be effective in reducing the impact of the risk.

- confidence test:** See *smoke test*.
- configuration:** The composition of a component or system as defined by the number, nature, and interconnections of its constituent parts.
- configuration auditing:** The function to check on the contents of libraries of configuration items, e.g. for standards compliance. [IEEE 610]
- configuration control:** An element of configuration management, consisting of the evaluation, co-ordination, approval or disapproval, and implementation of changes to configuration items after formal establishment of their configuration identification. [IEEE 610]
- configuration control board (CCB):** A group of people responsible for evaluating and approving or disapproving proposed changes to configuration items, and for ensuring implementation of approved changes. [IEEE 610]
- configuration identification:** An element of configuration management, consisting of selecting the configuration items for a system and recording their functional and physical characteristics in technical documentation. [IEEE 610]
- F-AT **configuration item:** An aggregation of hardware, software or both, that is designated for configuration management and treated as a single entity in the configuration management process. [IEEE 610]
- F  
F-AT **configuration management:** A discipline applying technical and administrative direction and surveillance to: identify and document the functional and physical characteristics of a configuration item, control changes to those characteristics, record and report change processing and implementation status, and verify compliance with specified requirements. [IEEE 610]
- F **configuration management tool:** A tool that provides support for the identification and control of configuration items, their status over changes and versions, and the release of baselines consisting of configuration items.
- configuration testing:** See *portability testing*.
- F **confirmation testing:** Testing that runs test cases that failed the last time they were run, in order to verify the success of corrective actions.
- conformance testing:** See *compliance testing*.
- consistency:** The degree of uniformity, standardization, and freedom from contradiction among the documents or parts of a component or system. [IEEE 610]
- ETM **consultative testing:** Testing driven by the advice and guidance of appropriate experts from outside the test team (e.g., technology experts and/or business domain experts).
- EITP **content-based model:** A process model providing a detailed description of good engineering practices, e.g. test practices.
- content reference model:** See *content-based model*.
- EITP **continuous representation:** A capability maturity model structure wherein capability levels provide a recommended order for approaching process improvement within specified process areas. [CMMI]
- ETM **control chart:** A statistical process control tool used to monitor a process and determine whether it is statistically controlled. It graphically depicts the average value and the upper and lower control limits (the highest and lowest values) of a process.

- F **control flow:** A sequence of events (paths) in the execution through a component or system.
- ATT **control flow analysis:** A form of static analysis based on a representation of unique paths (sequences of events) in the execution through a component or system. Control flow analysis evaluates the integrity of control flow structures, looking for possible control flow anomalies such as closed loops or logically unreachable process steps.
- control flow graph:** An abstract representation of all possible sequences of events (paths) in the execution through a component or system.
- control flow path:** See *path*.
- ATT **control flow testing:** An approach to structure-based testing in which test cases are designed to execute specific sequences of events. Various techniques exist for control flow testing, e.g., decision testing, condition testing, and path testing, that each have their specific approach and level of control flow coverage. See also decision testing, condition testing, path testing.
- ETM **convergence metric:** A metric that shows progress toward a defined criterion, e.g., convergence of the total number of test executed to the total number of tests planned for execution.
- conversion testing:** Testing of software used to convert data from existing systems for use in replacement systems.
- EITP **corporate dashboard:** A dashboard-style representation of the status of corporate performance data. See also *balanced scorecard*, *dashboard*.
- cost of quality:** The total costs incurred on quality activities and issues and often split into prevention costs, appraisal costs, internal failure costs and external failure costs.
- F **COTS:** Acronym for Commercial Off-The-Shelf software. See *off-the-shelf software*.
- coverage:** The degree, expressed as a percentage, to which a specified coverage item has been exercised by a test suite.
- coverage analysis:** Measurement of achieved coverage to a specified coverage item during test execution referring to predetermined criteria to determine whether additional testing is required and if so, which test cases are needed.
- coverage item:** An entity or property used as a basis for test coverage, e.g. equivalence partitions or code statements.
- coverage measurement tool:** See *coverage tool*.
- F **coverage tool:** A tool that provides objective measures of what structural elements, e.g. statements, branches have been exercised by a test suite.
- EITP **critical success factor:** An element necessary for an organization or project to achieve its mission. Critical success factors are the critical factors or activities required for ensuring the success.
- ATM **Critical Testing Processes:** A content-based model for test process improvement built  
EITP around twelve critical processes. These include highly visible processes, by which peers and management judge competence and mission-critical processes in which performance affects the company's profits and reputation. See also *content-based model*.
- ATM **CTP:** See *Critical Testing Processes*.  
EITP



**custom software:** See *bespoke software*.

ATM **custom tool:** A software tool developed specifically for a set of users or customers.

ATT **cyclomatic complexity:** The maximum number of linear, independent paths through a program. Cyclomatic complexity may be computed as:  $L - N + 2P$ , where

- L = the number of edges/links in a graph
- N = the number of nodes in a graph
- P = the number of disconnected parts of the graph (e.g. a called graph or subroutine)

[After McCabe]

**cyclomatic number:** See *cyclomatic complexity*.

## D

**daily build:** A development activity whereby a complete system is compiled and linked every day (often overnight), so that a consistent system is available at any time including all latest changes.

ETM **dashboard:** A representation of dynamic measurements of operational performance for some organization or activity, using metrics represented via metaphors such as visual 'dials', 'counters', and other devices resembling those on the dashboard of an automobile, so that the effects of events or activities can be easily understood and related to operational goals. See also *corporate dashboard*, *scorecard*.

**data definition:** An executable statement where a variable is assigned a value.

F **data-driven testing:** A scripting technique that stores test input and expected results in a  
ATT table or spreadsheet, so that a single control script can execute all of the tests in the table. Data-driven testing is often used to support the application of test execution tools such as capture/playback tools. [Fewster and Graham] See also *keyword-driven testing*.

F **data flow:** An abstract representation of the sequence and possible changes of the state of data objects, where the state of an object is any of: creation, usage, or destruction. [Beizer]

ATT **data flow analysis:** A form of static analysis based on the definition and usage of variables.

**data flow coverage:** The percentage of definition-use pairs that have been exercised by a test suite.

**data flow testing:** A white box test design technique in which test cases are designed to execute definition-use pairs of variables.

**data integrity testing:** See *database integrity testing*.

**data quality:** An attribute of data that indicates correctness with respect to some pre-defined criteria, e.g., business expectations, requirements on data integrity, data consistency.

**database integrity testing:** Testing the methods and processes used to access and manage the data(base), to ensure access methods, processes and data rules function as expected and that during access to the database, data is not corrupted or unexpectedly deleted, updated or created.

**dd-path:** A path between two decisions of an algorithm, or two decision nodes of a corresponding graph, that includes no other decisions. See also *path*.

**dead code:** See *unreachable code*.

**debugger:** See *debugging tool*.

- F **debugging:** The process of finding, analyzing and removing the causes of failures in software.
- F **debugging tool:** A tool used by programmers to reproduce failures, investigate the state of  
ATT programs and find the corresponding defect. Debuggers enable programmers to execute programs step by step, to halt a program at any program statement and to set and examine program variables.
- decision:** A program point at which the control flow has two or more alternative routes. A node with two or more links to separate branches.
- decision condition coverage:** The percentage of all condition outcomes and decision outcomes that have been exercised by a test suite. 100% decision condition coverage implies both 100% condition coverage and 100% decision coverage.
- ATT **decision condition testing:** A white box test design technique in which test cases are designed to execute condition outcomes and decision outcomes.
- F **decision coverage:** The percentage of decision outcomes that have been exercised by a test suite. 100% decision coverage implies both 100% branch coverage and 100% statement coverage.
- decision outcome:** The result of a decision (which therefore determines the branches to be taken).
- decision table:** A table showing combinations of inputs and/or stimuli (causes) with their associated outputs and/or actions (effects), which can be used to design test cases.
- F **decision table testing:** A black box test design technique in which test cases are designed to  
ATA execute the combinations of inputs and/or stimuli (causes) shown in a decision table. [Veenendaal04] See also *decision table*.
- decision testing:** A white box test design technique in which test cases are designed to execute decision outcomes.
- F **defect:** A flaw in a component or system that can cause the component or system to fail to  
ATM perform its required function, e.g. an incorrect statement or data definition. A defect, if encountered during execution, may cause a failure of the component or system.
- ATA **defect-based technique:** See *defect-based test design technique*.
- ATA **defect-based test design technique:** A procedure to derive and/or select test cases targeted at one or more defect types, with tests being developed from what is known about the specific defect type. See also *defect taxonomy*.
- defect category:** See *defect type*.
- F **defect density:** The number of defects identified in a component or system divided by the size of the component or system (expressed in standard measurement terms, e.g. lines-of-code, number of classes or function points).
- EITP **Defect Detection Percentage (DDP):** The number of defects found by a test level, divided by the number found by that test level and any other means afterwards. See also *escaped defects*.
- defect management:** The process of recognizing, investigating, taking action and disposing of defects. It involves recording defects, classifying them and identifying the impact. [After IEEE 1044]

ATM **defect management committee:** A cross-functional team of stakeholders who manage reported defects from initial detection to ultimate resolution (defect removal, defect deferral, or report cancellation). In some cases, the same team as the configuration control board. *See also* configuration control board.

**defect management tool:** A tool that facilitates the recording and status tracking of defects and changes. They often have workflow-oriented facilities to track and control the allocation, correction and re-testing of defects and provide reporting facilities. *See also* *incident management tool*.

**defect masking:** An occurrence in which one defect prevents the detection of another. [After IEEE 610]

**defect report:** A document reporting on any flaw in a component or system that can cause the component or system to fail to perform its required function. [After IEEE 829]

F-AT **defect taxonomy:** A system of (hierarchical) categories designed to be a useful aid for  
ATA reproducibly classifying defects.

**defect tracking tool:** *See* *defect management tool*.

ATM **defect triage committee:** *See* defect management committee.

**defect type:** An element in a taxonomy of defects. Defect taxonomies can be identified with respect to a variety of considerations, including, but not limited to:

- Phase or development activity in which the defect is created, e.g., a specification error or a coding error
- Characterization of defects, e.g., an “off-by-one” defect
- Incorrectness, e.g., an incorrect relational operator, a programming language syntax error, or an invalid assumption
- Performance issues, e.g., excessive execution time, insufficient availability.

ATT **definition-use pair:** The association of a definition of a variable with the subsequent use of that variable. Variable uses include computational (e.g. multiplication) or to direct the execution of a path (“predicate” use).

**deliverable:** Any (work) product that must be delivered to someone other than the (work) product’s author.

EITP **Deming cycle:** An iterative four-step problem-solving process, (plan-do-check-act), typically used in process improvement. [After Deming]

**design-based testing:** An approach to testing in which test cases are designed based on the architecture and/or detailed design of a component or system (e.g. tests of interfaces between components or systems).

**desk checking:** Testing of software or a specification by manual simulation of its execution. *See also* *static testing*.

**development testing:** Formal or informal testing conducted during the implementation of a component or system, usually in the development environment by developers. [After IEEE 610]

**deviation:** *See* *incident*.

**deviation report:** *See* *incident report*.

EITP **diagnosing (IDEAL):** The phase within the IDEAL model where it is determined where one is, relative to where one wants to be. The diagnosing phase consists of the activities: characterize current and desired states and develop recommendations. See also *IDEAL*.

**dirty testing:** See *negative testing*.

**documentation testing:** Testing the quality of the documentation, e.g. user guide or installation guide.

**domain:** The set from which valid input and/or output values can be selected.

ATA **domain analysis:** A black box test design technique that is used to identify efficient and effective test cases when multiple variables can or should be tested together. It builds on and generalizes equivalence partitioning and boundary values analysis. See also boundary value analysis, equivalence partitioning.

F **driver:** A software component or test tool that replaces a component that takes care of the control and/or the calling of a component or system. [After TMap]

ATT **dynamic analysis:** The process of evaluating behavior, e.g. memory performance, CPU usage, of a system or component during execution. [After IEEE 610]

F **dynamic analysis tool:** A tool that provides run-time information on the state of the software code. These tools are most commonly used to identify unassigned pointers, check pointer arithmetic and to monitor the allocation, use and de-allocation of memory and to flag memory leaks.

**dynamic comparison:** Comparison of actual and expected results, performed while the software is being executed, for example by a test execution tool.

F **dynamic testing:** Testing that involves the execution of the software of a component or system.

## E

ATM **effectiveness:** The capability of producing an intended result. See also *efficiency*.

ATM **efficiency:** (1) The capability of the software product to provide appropriate performance,  
ATT relative to the amount of resources used under stated conditions. [ISO 9126]

(2) The capability of a process to produce the intended outcome, relative to the amount of resources used

**efficiency testing:** The process of testing to determine the efficiency of a software product.

EITP **EFQM (European Foundation for Quality Management) excellence model:** A non-prescriptive framework for an organisation's quality management system, defined and owned by the European Foundation for Quality Management, based on five 'Enabling' criteria (covering what an organisation does), and four 'Results' criteria (covering what an organisation achieves).

**elementary comparison testing:** A black box test design technique in which test cases are designed to execute combinations of inputs using the concept of modified condition decision coverage. [TMap]

**embedded iterative development model:** A development lifecycle sub-model that applies an iterative approach to detailed design, coding and testing within an overall sequential model. In this case, the high level design documents are prepared and approved for the entire project but the actual detailed design, code development and testing are conducted in iterations.

- EITP **emotional intelligence:** The ability, capacity, and skill to identify, assess, and manage the emotions of one's self, of others, and of groups.
- emulator:** A device, computer program, or system that accepts the same inputs and produces the same outputs as a given system. [IEEE 610] See also *simulator*.
- F **entry criteria:** The set of generic and specific conditions for permitting a process to go forward with a defined task, e.g. test phase. The purpose of entry criteria is to prevent a task from starting which would entail more (wasted) effort compared to the effort needed to remove the failed entry criteria. [Gilb and Graham]
- entry point:** An executable statement or process step which defines a point at which a given process is intended to begin.
- equivalence class:** See *equivalence partition*.
- equivalence partition:** A portion of an input or output domain for which the behavior of a component or system is assumed to be the same, based on the specification.
- equivalence partition coverage:** The percentage of equivalence partitions that have been exercised by a test suite.
- F **equivalence partitioning:** A black box test design technique in which test cases are designed  
ATA to execute representatives from equivalence partitions. In principle test cases are designed to cover each partition at least once.
- F **error:** A human action that produces an incorrect result. [After IEEE 610]
- F **error guessing:** A test design technique where the experience of the tester is used to  
ATA anticipate what defects might be present in the component or system under test as a result of errors made, and to design tests specifically to expose them.
- error seeding:** See *fault seeding*.
- error seeding tool:** See *fault seeding tool*.
- error tolerance:** The ability of a system or component to continue normal operation despite the presence of erroneous inputs. [After IEEE 610].
- escaped defect:** A defect that was not detected in a previous test level which is supposed to find such type of defects. See also *Defect Detection Percentage*.
- EITP **establishing (IDEAL):** The phase within the IDEAL model where the specifics of how an organization will reach its destination are planned. The establishing phase consists of the activities: set priorities, develop approach and plan actions. See also *IDEAL*.
- evaluation:** See *testing*.
- exception handling:** Behavior of a component or system in response to erroneous input, from either a human user or from another component or system, or to an internal failure.
- executable statement:** A statement which, when compiled, is translated into object code, and which will be executed procedurally when the program is running and may perform an action on data.
- exercised:** A program element is said to be exercised by a test case when the input value causes the execution of that element, such as a statement, decision, or other structural element.
- F **exhaustive testing:** A test approach in which the test suite comprises all combinations of input values and preconditions.

F **exit criteria:** The set of generic and specific conditions, agreed upon with the stakeholders  
ATM for permitting a process to be officially completed. The purpose of exit criteria is to  
ATA prevent a task from being considered completed when there are still outstanding parts of  
the task which have not been finished. Exit criteria are used to report against and to plan  
when to stop testing. [After Gilb and Graham]

**exit point:** An executable statement or process step which defines a point at which a given  
process is intended to cease..

**expected outcome:** See *expected result*.

**expected result:** The behavior predicted by the specification, or another source, of the  
component or system under specified conditions.

ATA **experience-based technique:** See *experience-based test design technique*.

F **experience-based test design technique:** Procedure to derive and/or select test cases based  
ATA on the tester's experience, knowledge and intuition.

**experience-based testing:** Testing based on the tester's experience, knowledge and intuition.

F **exploratory testing:** An informal test design technique where the tester actively controls the  
F-AT design of the tests as those tests are performed and uses information gained while testing to  
ATA design new and better tests. [After Bach]

EITP **extreme programming (XP):** A software engineering methodology used within agile  
software development whereby core practices are programming in pairs, doing extensive  
code review, unit testing of all code, and simplicity and clarity in code. See also *agile  
software development*.

## F

**factory acceptance testing:** Acceptance testing conducted at the site at which the product is  
developed and performed by employees of the supplier organization, to determine whether  
or not a component or system satisfies the requirements, normally including hardware as  
well as software. See also *alpha testing*.

**fail:** A test is deemed to fail if its actual result does not match its expected result.

**failover testing:** Testing by simulating failure modes or actually causing failures in a  
controlled environment. Following a failure, the failover mechanism is tested to ensure that  
data is not lost or corrupted and that any agreed service levels are maintained (e.g.,  
function availability or response times). See also *recoverability testing*.

F **failure:** Deviation of the component or system from its expected delivery, service or result.  
ATM [After Fenton]

**failure mode:** The physical or functional manifestation of a failure. For example, a system in  
failure mode may be characterized by slow operation, incorrect outputs, or complete  
termination of execution. [IEEE 610]

EITP **Failure Mode and Effect Analysis (FMEA):** A systematic approach to risk identification  
and analysis of identifying possible modes of failure and attempting to prevent their  
occurrence. See also *Failure Mode, Effect and Criticality Analysis (FMECA)*.

**Failure Mode, Effects, and Criticality Analysis (FMECA):** An extension of FMEA, as in  
addition to the basic FMEA, it includes a criticality analysis, which is used to chart the  
probability of failure modes against the severity of their consequences. The result  
highlights failure modes with relatively high probability and severity of consequences,

allowing remedial effort to be directed where it will produce the greatest value. See also *Failure Mode and Effect Analysis (FMEA)*.

F **failure rate:** The ratio of the number of failures of a given category to a given unit of measure, e.g. failures per unit of time, failures per number of transactions, failures per number of computer runs. [IEEE 610]

ATM **false-fail result:** A test result in which a defect is reported although no such defect actually exists in the test object.

ATM **false-negative result:** See *false-pass result*.

ATM **false-pass result:** A test result which fails to identify the presence of a defect that is actually present in the test object.

ATM **false-positive result:** See *false-fail result*.

F **fault:** See *defect*.

F **fault attack:** See *attack*.

**fault density:** See *defect density*.

**Fault Detection Percentage (FDP):** See *Defect Detection Percentage (DDP)*.

**fault injection:** The process of intentionally adding defects to a system for the purpose of finding out whether the system can detect, and possibly recover from, a defect. Fault injection intended to mimic failures that might occur in the field. See also *fault tolerance*.

**fault masking:** See *defect masking*.

**fault seeding:** The process of intentionally adding defects to those already in the component or system for the purpose of monitoring the rate of detection and removal, and estimating the number of remaining defects. Fault seeding is typically part of development (pre-release) testing and can be performed at any test level (component, integration, or system). [After IEEE 610]

ATT **fault seeding tool:** A tool for seeding (i.e. intentionally inserting) faults in a component or system.

**fault tolerance:** The capability of the software product to maintain a specified level of performance in cases of software faults (defects) or of infringement of its specified interface. [ISO 9126] See also *reliability, robustness*.

EITP **Fault Tree Analysis (FTA):** A technique used to analyze the causes of faults (defects). The technique visually models how logical relationships between failures, human errors, and external events can combine to cause specific faults to disclose.

**feasible path:** A path for which a set of input values and preconditions exists which causes it to be executed.

**feature:** An attribute of a component or system specified or implied by requirements documentation (for example reliability, usability or design constraints). [After IEEE 1008]

ETM **feature-driven development:** An iterative and incremental software development process driven from a client-valued functionality (feature) perspective. Feature-driven development is mostly used in agile software development. See also *agile software development*.

F **field testing:** See *beta testing*.

**finite state machine:** A computational model consisting of a finite number of states and transitions between those states, possibly with accompanying actions. [IEEE 610]

**finite state testing:** See *state transition testing*.

**fishbone diagram:** See *cause-effect diagram*.

F **formal review:** A review characterized by documented procedures and requirements, e.g. inspection.

**frozen test basis:** A test basis document that can only be amended by a formal change control process. See also *baseline*.

**Function Point Analysis (FPA):** Method aiming to measure the size of the functionality of an information system. The measurement is independent of the technology. This measurement may be used as a basis for the measurement of productivity, the estimation of the needed resources, and project control.

**functional integration:** An integration approach that combines the components or systems for the purpose of getting a basic functionality working early. See also *integration testing*.

F **functional requirement:** A requirement that specifies a function that a component or system must perform. [IEEE 610]

**functional test design technique:** Procedure to derive and/or select test cases based on an analysis of the specification of the functionality of a component or system without reference to its internal structure. See also *black box test design technique*.

F **functional testing:** Testing based on an analysis of the specification of the functionality of a component or system. See also *black box testing*.

**functionality:** The capability of the software product to provide functions which meet stated and implied needs when the software is used under specified conditions. [ISO 9126]

**functionality testing:** The process of testing to determine the functionality of a software product.

## G

**glass box testing:** See *white box testing*.

EITP **Goal Question Metric:** An approach to software measurement using a three-level model: conceptual level (goal), operational level (question) and quantitative level (metric).

EITP **GQM:** See *Goal Question Metric*.

## H

**hardware-software integration testing:** Testing performed to expose defects in the interfaces and interaction between hardware and software components. See also *integration testing*.

**hazard analysis:** A technique used to characterize the elements of risk. The result of a hazard analysis will drive the methods used for development and testing of a system. See also *risk analysis*.

ATA **heuristic evaluation:** A usability review technique that targets usability problems in the user interface or user interface design. With this technique, the reviewers examine the interface and judge its compliance with recognized usability principles (the "heuristics").

ATA **high level test case:** A test case without concrete (implementation level) values for input data and expected results. Logical operators are used; instances of the actual values are not yet defined and/or available. See also *low level test case*.



**horizontal traceability:** The tracing of requirements for a test level through the layers of test documentation (e.g. test plan, test design specification, test case specification and test procedure specification or test script).

**hyperlink:** A pointer within a web page that leads to other web pages.

ATT **hyperlink test tool:** A tool used to check that no broken hyperlinks are present on a web site.

## I

EITP **IDEAL:** An organizational improvement model that serves as a roadmap for initiating, planning, and implementing improvement actions. The IDEAL model is named for the five phases it describes: initiating, diagnosing, establishing, acting, and learning.

F **impact analysis:** The assessment of change to the layers of development documentation, test documentation and components, in order to implement a given change to specified requirements.

F **incident:** Any event occurring that requires investigation. [After IEEE 1008]

F **incident logging:** Recording the details of any incident that occurred, e.g. during testing.

F **incident management:** The process of recognizing, investigating, taking action and disposing of incidents. It involves logging incidents, classifying them and identifying the impact. [After IEEE 1044]

F **incident management tool:** A tool that facilitates the recording and status tracking of incidents. They often have workflow-oriented facilities to track and control the allocation, correction and re-testing of incidents and provide reporting facilities. See also *defect management tool*.

F **incident report:** A document reporting on any event that occurred, e.g. during the testing, which requires investigation. [After IEEE 829]

F  
F-AT **incremental development model:** A development lifecycle where a project is broken into a series of increments, each of which delivers a portion of the functionality in the overall project requirements. The requirements are prioritized and delivered in priority order in the appropriate increment. In some (but not all) versions of this lifecycle model, each subproject follows a 'mini V-model' with its own design, coding and testing phases.

**incremental testing:** Testing where components or systems are integrated and tested one or some at a time, until all the components or systems are integrated and tested.

F  
ATM **independence of testing:** Separation of responsibilities, which encourages the accomplishment of objective testing. [After DO-178b]

EITP **indicator:** A measure that can be used to estimate or predict another measure. [ISO 14598]

**infeasible path:** A path that cannot be exercised by any set of possible input values.

F  
ATM **informal review:** A review not based on a formal (documented) procedure.

EITP **initiating (IDEAL):** The phase within the IDEAL model where the groundwork is laid for a successful improvement effort. The initiating phase consists of the activities: set context, build sponsorship and charter infrastructure. See also *IDEAL*.

**input:** A variable (whether stored within a component or outside) that is read by a component.

**input domain:** The set from which valid input values can be selected. See also *domain*.

- input value:** An instance of an input. See also *input*.
- insourced testing:** Testing performed by people who are co-located with the project team but are not fellow employees.
- F **inspection:** A type of peer review that relies on visual examination of documents to detect  
 ATM defects, e.g. violations of development standards and non-conformance to higher level  
 EITP documentation. The most formal review technique and therefore always based on a  
 documented procedure. [After IEEE 610, IEEE 1028] See also *peer review*.
- inspection leader:** See *moderator*.
- inspector:** See *reviewer*.
- ATT **installability:** The capability of the software product to be installed in a specified  
 environment [ISO 9126]. See also *portability*.
- installability testing:** The process of testing the installability of a software product. See also  
*portability testing*.
- installation guide:** Supplied instructions on any suitable media, which guides the installer  
 through the installation process. This may be a manual guide, step-by-step procedure,  
 installation wizard, or any other similar process description.
- installation wizard:** Supplied software on any suitable media, which leads the installer  
 through the installation process. It normally runs the installation process, provides  
 feedback on installation results, and prompts for options.
- instrumentation:** The insertion of additional code into the program in order to collect  
 information about program behavior during execution, e.g. for measuring code coverage.
- instrumenter:** A software tool used to carry out instrumentation.
- intake test:** A special instance of a smoke test to decide if the component or system is ready  
 for detailed and further testing. An intake test is typically carried out at the start of the test  
 execution phase. See also *smoke test*.
- F **integration:** The process of combining components or systems into larger assemblies.
- F **integration testing:** Testing performed to expose defects in the interfaces and in the  
 interactions between integrated components or systems. See also *component integration  
 testing, system integration testing*.
- integration testing in the large:** See *system integration testing*.
- integration testing in the small:** See *component integration testing*.
- interface testing:** An integration test type that is concerned with testing the interfaces  
 between components or systems.
- interoperability:** The capability of the software product to interact with one or more  
 specified components or systems. [After ISO 9126] See also *functionality*.
- F **interoperability testing:** The process of testing to determine the interoperability of a  
 ATA software product. See also *functionality testing*.
- invalid testing:** Testing using input values that should be rejected by the component or  
 system. See also *error tolerance, negative testing*.
- ETM **Ishikawa diagram:** See *cause-effect diagram*.

**isolation testing:** Testing of individual components in isolation from surrounding components, with surrounding components being simulated by stubs and drivers, if needed.

**item transmittal report:** See *release note*.

F **iterative development model:** A development lifecycle where a project is broken into a  
F-AT usually large number of iterations. An iteration is a complete development loop resulting in a release (internal or external) of an executable product, a subset of the final product under development, which grows from iteration to iteration to become the final product.

## K

**key performance indicator:** See *performance indicator*.

F **keyword-driven testing:** A scripting technique that uses data files to contain not only test  
ATA data and expected results, but also keywords related to the application being tested. The  
ATT keywords are interpreted by special supporting scripts that are called by the control script for the test. See also *data-driven testing*.

## L

**LCSAJ:** A Linear Code Sequence And Jump, consists of the following three items (conventionally identified by line numbers in a source code listing): the start of the linear sequence of executable statements, the end of the linear sequence, and the target line to which control flow is transferred at the end of the linear sequence.

**LCSAJ coverage:** The percentage of LCSAJs of a component that have been exercised by a test suite. 100% LCSAJ coverage implies 100% decision coverage.

**LCSAJ testing:** A white box test design technique in which test cases are designed to execute LCSAJs.

EITP **lead assessor:** The person who leads an assessment. In some cases, for instance CMMi and TMMi when formal assessments are conducted, the lead assessor must be accredited and formally trained.

ATA **learnability:** The capability of the software product to enable the user to learn its application. [ISO 9126] See also *usability*.

EITP **learning (IDEAL):** The phase within the IDEAL model where one learns from experiences and improves one's ability to adopt new processes and technologies in the future. The learning phase consists of the activities: analyze and validate, and propose future actions. See also *IDEAL*.

ATM **level test plan:** A test plan that typically addresses one test level. See also *test plan*.

EITP **lifecycle model:** A partitioning of the life of a product or project into phases. [CMMI] See also *software lifecycle*.

**link testing:** See *component integration testing*.

**load profile:** A specification of the activity which a component or system being tested may experience in production. A load profile consists of a designated number of virtual users who process a defined set of transactions in a specified time period and according to a predefined operational profile. See also *operational profile*.

F **load testing:** A type of performance testing conducted to evaluate the behavior of a component or system with increasing load, e.g. numbers of parallel users and/or numbers

of transactions, to determine what load can be handled by the component or system. See also *performance testing*, *stress testing*.

F **load testing tool:** A tool to support load testing whereby it can simulate increasing load, e.g., numbers of concurrent users and/or transactions within a specified time-period. See also *performance testing tool*.

**logic-coverage testing:** See *white box testing*. [Myers]

**logic-driven testing:** See *white box testing*.

ATA **logical test case:** See *high level test case*.

ATA **low level test case:** A test case with concrete (implementation level) values for input data and expected results. Logical operators from high level test cases are replaced by actual values that correspond to the objectives of the logical operators. See also *high level test case*.

## M

**maintainability:** The ease with which a software product can be modified to correct defects, modified to meet new requirements, modified to make future maintenance easier, or adapted to a changed environment. [ISO 9126]

F **maintainability testing:** The process of testing to determine the maintainability of a software  
F-AT product.  
ATT

**maintenance:** Modification of a software product after delivery to correct defects, to improve performance or other attributes, or to adapt the product to a modified environment. [IEEE 1219]

F **maintenance testing:** Testing the changes to an operational system or the impact of a changed environment to an operational system.

**man in the middle attack:** The interception, mimicking and/or altering and subsequent relaying of communications (e.g., credit card transactions) by a third party such that a user remains unaware of that third party's presence.

ATM **management review:** A systematic evaluation of software acquisition, supply, development, operation, or maintenance process, performed by or on behalf of management that monitors progress, determines the status of plans and schedules, confirms requirements and their system allocation, or evaluates the effectiveness of management approaches to achieve fitness for purpose. [After IEEE 610, IEEE 1028]

EITP **manufacturing-based quality:** A view of quality, whereby quality is measured by the degree to which a product or service conforms to its intended design and requirements. Quality arises from the process(es) used. [After Garvin] See also *product-based quality*, *transcendent-based quality*, *user-based quality*, *value-based quality*.

ATM **master test plan:** A test plan that typically addresses multiple test levels. See also *test plan*.

ATT **maturity:** (1) The capability of an organization with respect to the effectiveness and efficiency of its processes and work practices. See also *Capability Maturity Model Integration*, *Test Maturity Model integration*.

(2) The capability of the software product to avoid failure as a result of defects in the software. [ISO 9126] See also *reliability*.

EITP **maturity level:** Degree of process improvement across a predefined set of process areas in which all goals in the set are attained. [TMMi]

**maturity model:** A structured collection of elements that describe certain aspects of maturity in an organization, and aid in the definition and understanding of an organization's processes. A maturity model often provides a common language, shared vision and framework for prioritizing improvement actions.

**MCDC:** See *modified condition decision coverage*.

**Mean Time Between Failures:** The arithmetic mean (average) time between failures of a system. The MTBF is typically part of a reliability growth model that assumes the failed system is immediately repaired, as a part of a defect fixing process. See also *reliability growth model*.

**Mean Time To Repair:** The arithmetic mean (average) time a system will take to recover from any failure. This typically includes testing to insure that the defect has been resolved.

EITP **measure:** The number or category assigned to an attribute of an entity by making a measurement. [ISO 14598]

**measurement:** The process of assigning a number or category to an entity to describe an attribute of that entity. [ISO 14598]

**measurement scale:** A scale that constrains the type of data analysis that can be performed on it. [ISO 14598]

ATT **memory leak:** A memory access failure due to a defect in a program's dynamic store allocation logic that causes it to fail to release memory after it has finished using it, eventually causing the program and/or other concurrent processes to fail due to lack of memory.

ETM **methodical testing:** Testing based on a standard set of tests, e.g., a checklist, a quality standard, or a set of generalized test cases.

F **metric:** A measurement scale and the method used for measurement. [ISO 14598]  
EITP

**migration testing:** See *conversion testing*.

**milestone:** A point in time in a project at which defined (intermediate) deliverables and results should be ready.

EITP **mind map:** A diagram used to represent words, ideas, tasks, or other items linked to and arranged around a central keyword or idea. Mind maps are used to generate, visualize, structure, and classify ideas, and as an aid in study, organization, problem solving, decision making, and writing.

F **mistake:** See *error*.

ETM **model-based testing:** Testing based on a model of the component or system under test, e.g., reliability growth models, usage models such as operational profiles or behavioral models such as decision table or state transition diagram.

F **modeling tool:** A tool that supports the creation, amendment and verification of models of the software or system [Graham].

F **moderator:** The leader and main person responsible for an inspection or other review  
ATM process.

**modified condition decision coverage:** The percentage of all single condition outcomes that independently affect a decision outcome that have been exercised by a test case suite. 100% modified condition decision coverage implies 100% decision condition coverage.

**modified condition decision testing:** A white box test design technique in which test cases are designed to execute single condition outcomes that independently affect a decision outcome.

**modified multiple condition coverage:** See *modified condition decision coverage*.

**modified multiple condition testing:** See *modified condition decision testing*.

**module:** See *component*.

**module testing:** See *component testing*.

**monitor:** A software tool or hardware device that runs concurrently with the component or system under test and supervises, records and/or analyses the behavior of the component or system. [After IEEE 610]

F **monitoring tool:** See *monitor*.

**monkey testing:** Testing by means of a random selection from a large range of inputs and by randomly pushing buttons, ignorant of how the product is being used.

**MTBF:** See *Mean Time Between Failures*.

**MTTR:** See *Mean Time To Repair*.

**multiple condition:** See *compound condition*.

**multiple condition coverage:** The percentage of combinations of all single condition outcomes within one statement that have been exercised by a test suite. 100% multiple condition coverage implies 100% modified condition decision coverage.

ATT **multiple condition testing:** A white box test design technique in which test cases are designed to execute combinations of single condition outcomes (within one statement).

**mutation analysis:** A method to determine test suite thoroughness by measuring the extent to which a test suite can discriminate the program from slight variants (mutants) of the program.

**mutation testing:** See *back-to-back testing*.

ETM **Myers-Briggs Type Indicator (MBTI):** An indicator of psychological preference representing the different personalities and communication styles of people.

## N

**N-switch coverage:** The percentage of sequences of N+1 transitions that have been exercised by a test suite. [Chow]

**N-switch testing:** A form of state transition testing in which test cases are designed to execute all valid sequences of N+1 transitions. [Chow] See also *state transition testing*.

**n-wise testing:** A black box test design technique in which test cases are designed to execute all possible discrete combinations of any set of n input parameters. See also *combinatorial testing, orthogonal array testing, pairwise testing*.

**negative testing:** Tests aimed at showing that a component or system does not work. Negative testing is related to the testers' attitude rather than a specific test approach or test design technique, e.g. testing with invalid input values or exceptions. [After Beizer].

ATT **neighborhood integration testing:** A form of integration testing where all of the nodes that connect to a given node are the basis for the integration testing.

**non-conformity:** Non fulfillment of a specified requirement. [ISO 9000]

F **non-functional requirement:** A requirement that does not relate to functionality, but to attributes such as reliability, efficiency, usability, maintainability and portability.

**non-functional test design technique:** Procedure to derive and/or select test cases for non-functional testing based on an analysis of the specification of a component or system without reference to its internal structure. See also *black box test design technique*.

**non-functional testing:** Testing the attributes of a component or system that do not relate to functionality, e.g. reliability, efficiency, usability, maintainability and portability.

## O

**off-the-shelf software:** A software product that is developed for the general market, i.e. for a large number of customers, and that is delivered to many customers in identical format.

ATM **open source tool:** A software tool that is available to all potential users in source code form, usually via the internet; its users are permitted, usually under license, to study, change, improve and, at times, to distribute the software.

ATA **operability:** The capability of the software product to enable the user to operate and control it. [ISO 9126] See also *usability*.

ATT **operational acceptance testing:** Operational testing in the acceptance test phase, typically performed in a (simulated) operational environment by operations and/or systems administration staff focusing on operational aspects, e.g. recoverability, resource-behavior, installability and technical compliance. See also *operational testing*.

**operational environment:** Hardware and software products installed at users' or customers' sites where the component or system under test will be used. The software may include operating systems, database management systems, and other applications.

ETM **operational profile:** The representation of a distinct set of tasks performed by the component  
ATT or system, possibly based on user behavior when interacting with the component or system, and their probabilities of occurrence. A task is logical rather than physical and can be executed over several machines or be executed in non-contiguous time segments.

**operational profile testing:** Statistical testing using a model of system operations (short duration tasks) and their probability of typical use. [Musa]

ETM **operational profiling:** The process of developing and implementing an operational profile. See also *operational profile*.

**operational testing:** Testing conducted to evaluate a component or system in its operational environment. [IEEE 610]

**oracle:** See *test oracle*.

ATA **orthogonal array:** A 2-dimensional array constructed with special mathematical properties, such that choosing any two columns in the array provides every pair combination of each number in the array.

A-TA **orthogonal array testing:** A systematic way of testing all-pair combinations of variables using orthogonal arrays. It significantly reduces the number of all combinations of variables to test all pair combinations. See also *combinatorial testing*, *n-wise testing*, *pairwise testing*.

**outcome:** See *result*.

**output:** A variable (whether stored within a component or outside) that is written by a component.

**output domain:** The set from which valid output values can be selected. See also *domain*.

**output value:** An instance of an output. See also *output*.

**outsourced testing:** Testing performed by people who are not co-located with the project team and are not fellow employees.

## P

**pair programming:** A software development approach whereby lines of code (production and/or test) of a component are written by two programmers sitting at a single computer. This implicitly means ongoing real-time code reviews are performed.

**pair testing:** Two persons, e.g. two testers, a developer and a tester, or an end-user and a tester, working together to find defects. Typically, they share one computer and trade control of it while testing.

ATT **pairwise integration testing:** A form of integration testing that targets pairs of components that work together, as shown in a call graph.

ATA **pairwise testing:** A black box test design technique in which test cases are designed to execute all possible discrete combinations of each pair of input parameters. See also *combinatorial testing*, *n-wise testing*, *orthogonal array testing*.

EITP **Pareto analysis:** A statistical technique in decision making that is used for selection of a limited number of factors that produce significant overall effect. In terms of quality improvement, a large majority of problems (80%) are produced by a few key causes (20%).

**partition testing:** See *equivalence partitioning*. [Beizer]

**pass:** A test is deemed to pass if its actual result matches its expected result.

**pass/fail criteria:** Decision rules used to determine whether a test item (function) or feature has passed or failed a test. [IEEE 829]

**path:** A sequence of events, e.g. executable statements, of a component or system from an entry point to an exit point.

**path coverage:** The percentage of paths that have been exercised by a test suite. 100% path coverage implies 100% LCSAJ coverage.

**path sensitizing:** Choosing a set of input values to force the execution of a given path.

ATT **path testing:** A white box test design technique in which test cases are designed to execute paths.

F **peer review:** A review of a software work product by colleagues of the producer of the product for the purpose of identifying defects and improvements. Examples are inspection, technical review and walkthrough.

**performance:** The degree to which a system or component accomplishes its designated functions within given constraints regarding processing time and throughput rate. [After IEEE 610] See also *efficiency*.

**performance indicator:** A high level metric of effectiveness and/or efficiency used to guide and control progressive development, e.g. lead-time slip for software development. [CMMI]



- performance profiling:** The task of analyzing, e.g., identifying performance bottlenecks based on generated metrics, and tuning the performance of a software component or system using tools.
- F **performance testing:** The process of testing to determine the performance of a software  
F-AT product. See also *efficiency testing*.  
ATT
- F **performance testing tool:** A tool to support performance testing that usually has two main  
ATT facilities: load generation and test transaction measurement. Load generation can simulate either multiple users or high volumes of input data. During execution, response time measurements are taken from selected transactions and these are logged. Performance testing tools normally provide reports based on test logs and graphs of load against response times.
- ATA **phase containment:** The percentage of defects that are removed in the same phase of the  
ATM software lifecycle in which they were introduced.
- phase test plan:** A test plan that typically addresses one test phase. See also *test plan*.
- ETM **planning poker:** A consensus-based estimation technique, mostly used to estimate effort or  
relative size of user stories in agile software development. It is a variation of the Wide Band Delphi method using a deck of cards with values representing the units in which the team estimates. See also *agile software development*, *Wide Band Delphi*.
- pointer:** A data item that specifies the location of another data item; for example, a data item that specifies the address of the next employee record to be processed. [IEEE 610]
- portability:** The ease with which the software product can be transferred from one hardware or software environment to another. [ISO 9126]
- F **portability testing:** The process of testing to determine the portability of a software product.  
ATT
- postcondition:** Environmental and state conditions that must be fulfilled after the execution of a test or test procedure.
- post-execution comparison:** Comparison of actual and expected results, performed after the software has finished running.
- post-project meeting:** See *retrospective meeting*.
- precondition:** Environmental and state conditions that must be fulfilled before the component or system can be executed with a particular test or test procedure.
- predicate:** A statement that can evaluate to true or false and may be used to determine the control flow of subsequent decision logic. See also *decision*.
- predicted outcome:** See *expected result*.
- pretest:** See *intake test*.
- ATM **priority:** The level of (business) importance assigned to an item, e.g. defect.
- PRISMA (Product RiSk Management):** A systematic approach to risk-based testing that employs product risk identification and analysis to create a product risk matrix based on likelihood and impact.
- F **probe effect:** The effect on the component or system by the measurement instrument when  
the component or system is being measured, e.g. by a performance testing tool or monitor.

For example performance may be slightly worse when performance testing tools are being used.

**problem:** See *defect*.

**problem management:** See *defect management*.

**problem report:** See *defect report*.

**procedure testing:** Testing aimed at ensuring that the component or system can operate in conjunction with new or existing users' business procedures or operational procedures.

**process:** A set of interrelated activities, which transform inputs into outputs. [ISO 12207]

EITP **process assessment:** A disciplined evaluation of an organization's software processes against a reference model. [after ISO 15504]

ETM **process-compliant testing:** Testing that follows a set of defined processes, e.g., defined by an external party such as a standards committee. See also *standard-compliant testing*.

**process cycle test:** A black box test design technique in which test cases are designed to execute business procedures and processes. [TMap] See also *procedure testing*.

**process improvement:** A program of activities designed to improve the performance and maturity of the organization's processes, and the result of such a program. [CMMI]

EITP **process model:** A framework wherein processes of the same nature are classified into a overall model, e.g. a test improvement model.

**process reference model:** A process model providing a generic body of best practices and how to improve a process in a prescribed step-by-step manner.

EITP **product-based quality:** A view of quality, wherein quality is based on a well-defined set of quality attributes. These attributes must be measured in an objective and quantitative way. Differences in the quality of products of the same type can be traced back to the way the specific quality attributes have been implemented. [After Garvin] See also *manufacturing-based quality, quality attribute, transcendent-based quality, user-based quality, value-based quality*.

F **product risk:** A risk directly related to the test object. See also *risk*.

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**Product RiSk Management:** See *PRISMA*.

**production acceptance testing:** See *operational acceptance testing*.

**program instrumenter:** See *instrumenter*.

**program testing:** See *component testing*.

**project:** A project is a unique set of coordinated and controlled activities with start and finish dates undertaken to achieve an objective conforming to specific requirements, including the constraints of time, cost and resources. [ISO 9000]

EITP **project retrospective:** A structured way to capture lessons learned and to create specific action plans for improving on the next project or next project phase.

F **project risk:** A risk related to management and control of the (test) project, e.g. lack of  
ATM staffing, strict deadlines, changing requirements, etc. See also *risk*.

**project test plan:** See *master test plan*.

**pseudo-random:** A series which appears to be random but is in fact generated according to some prearranged sequence.

## Q

**QFD:** See *quality function deployment*.

**qualification:** The process of demonstrating the ability to fulfill specified requirements. Note the term ‘qualified’ is used to designate the corresponding status. [ISO 9000]

F **quality:** The degree to which a component, system or process meets specified requirements and/or user/customer needs and expectations. [After IEEE 610]

**quality assurance:** Part of quality management focused on providing confidence that quality requirements will be fulfilled. [ISO 9000]

**quality attribute:** A feature or characteristic that affects an item’s quality. [IEEE 610]

**quality characteristic:** See *quality attribute*.

**quality control:** The operational techniques and activities, part of quality management, that are focused on fulfilling quality requirements. [after ISO 8402]

**quality function deployment:** A method to transform user demands into design quality, to deploy the functions forming quality, and to deploy methods for achieving the design quality into subsystems and component parts, and ultimately to specific elements of the manufacturing process. [Akao]

**quality gate:** A special milestone in a project. Quality gates are located between those phases of a project strongly depending on the outcome of a previous phase. A quality gate includes a formal check of the documents of the previous phase.

**quality management:** Coordinated activities to direct and control an organization with regard to quality. Direction and control with regard to quality generally includes the establishment of the quality policy and quality objectives, quality planning, quality control, quality assurance and quality improvement. [ISO 9000]

F-AT **quality risk:** A product risk related to a quality attribute. See also *quality attribute, product*  
ATM *risk*.

## R

ETM **RACI matrix:** A matrix describing the participation by various roles in completing tasks or deliverables for a project or process. It is especially useful in clarifying roles and responsibilities. RACI is an acronym derived from the four key responsibilities most typically used: Responsible, Accountable, Consulted, and Informed.

**random testing:** A black box test design technique where test cases are selected, possibly using a pseudo-random generation algorithm, to match an operational profile. This technique can be used for testing non-functional attributes such as reliability and performance.

EITP **Rational Unified Process:** A proprietary adaptable iterative software development process framework consisting of four project lifecycle phases: inception, elaboration, construction and transition.

- ETM **reactive testing:** Testing that dynamically responds to the actual system under test and test results being obtained. Typically reactive testing has a reduced planning cycle and the design and implementation test phases are not carried out until the test object is received.
- recorder:** See *scribe*.
- ATT **record/playback tool:** See *capture/playback tool*.
- recoverability:** The capability of the software product to re-establish a specified level of performance and recover the data directly affected in case of failure. [ISO 9126] See also *reliability*.
- ATT **recoverability testing:** The process of testing to determine the recoverability of a software product. See also *reliability testing*.
- recovery testing:** See *recoverability testing*.
- ETM **regression-averse testing:** Testing using various techniques to manage the risk of regression, e.g., by designing re-usable testware and by extensive automation of testing at one or more test levels.
- F **regression testing:** Testing of a previously tested program following modification to ensure  
F-AT that defects have not been introduced or uncovered in unchanged areas of the software, as a result of the changes made. It is performed when the software or its environment is changed.
- regulation testing:** See *compliance testing*.
- release note:** A document identifying test items, their configuration, current status and other delivery information delivered by development to testing, and possibly other stakeholders, at the start of a test execution phase. [After IEEE 829]
- reliability:** The ability of the software product to perform its required functions under stated conditions for a specified period of time, or for a specified number of operations. [ISO 9126]
- ATT **reliability growth model:** A model that shows the growth in reliability over time during continuous testing of a component or system as a result of the removal of defects that result in reliability failures.
- F **reliability testing:** The process of testing to determine the reliability of a software product.  
ATT
- ATT **replaceability:** The capability of the software product to be used in place of another specified software product for the same purpose in the same environment. [ISO 9126] See also *portability*.
- F **requirement:** A condition or capability needed by a user to solve a problem or achieve an objective that must be met or possessed by a system or system component to satisfy a contract, standard, specification, or other formally imposed document. [After IEEE 610]
- ATA **requirements-based testing:** An approach to testing in which test cases are designed based on test objectives and test conditions derived from requirements, e.g. tests that exercise specific functions or probe non-functional attributes such as reliability or usability.
- F **requirements management tool:** A tool that supports the recording of requirements, requirements attributes (e.g. priority, knowledge responsible) and annotation, and facilitates traceability through layers of requirements and requirements change

management. Some requirements management tools also provide facilities for static analysis, such as consistency checking and violations to pre-defined requirements rules.

**requirements phase:** The period of time in the software lifecycle during which the requirements for a software product are defined and documented. [IEEE 610]

**resource utilization:** The capability of the software product to use appropriate amounts and types of resources, for example the amounts of main and secondary memory used by the program and the sizes of required temporary or overflow files, when the software performs its function under stated conditions. [After ISO 9126] See also *efficiency*.

ATT **resource utilization testing:** The process of testing to determine the resource-utilization of a software product. See also *efficiency testing*.

**result:** The consequence/outcome of the execution of a test. It includes outputs to screens, changes to data, reports, and communication messages sent out. See also *actual result*, *expected result*.

**resumption criteria:** The criteria used to restart all or a portion of the testing activities that were suspended previously.

**resumption requirements:** The defined set of testing activities that must be repeated when testing is re-started after a suspension. [After IEEE 829]

F **re-testing:** See *confirmation testing*.

EITP **retrospective meeting:** A meeting at the end of a project during which the project team members evaluate the project and learn lessons that can be applied to the next project.

F **review:** An evaluation of a product or project status to ascertain discrepancies from planned results and to recommend improvements. Examples include management review, informal review, technical review, inspection, and walkthrough. [After IEEE 1028]

ATM **review plan:** A document describing the approach, resources and schedule of intended review activities. It identifies, amongst others: documents and code to be reviewed, review types to be used, participants, as well as entry and exit criteria to be applied in case of formal reviews, and the rationale for their choice. It is a record of the review planning process.

F **review tool:** A tool that provides support to the review process. Typical features include review planning and tracking support, communication support, collaborative reviews and a repository for collecting and reporting of metrics.

F **reviewer:** The person involved in the review that identifies and describes anomalies in the product or project under review. Reviewers can be chosen to represent different viewpoints and roles in the review process.

F **risk:** A factor that could result in future negative consequences; usually expressed as impact and likelihood.

ATM **risk analysis:** The process of assessing identified project or product risks to determine their level of risk, typically by estimating their impact and probability of occurrence

ATA (likelihood)

ATT **risk assessment:** The process of identifying and subsequently analyzing the identified project or product risk to determine its level of risk, typically by assigning likelihood and impact ratings. *See also* product risk, project risk, risk, risk impact, risk level, risk likelihood.

F **risk-based testing:** An approach to testing to reduce the level of product risks and inform

ATM stakeholders of their status, starting in the initial stages of a project. It involves the  
ATA identification of product risks and the use of risk levels to guide the test process.  
ATT

**risk category:** See *risk type*.

ATM **risk control:** The process through which decisions are reached and protective measures are  
ATA implemented for reducing risks to, or maintaining risks within, specified levels.  
ATT

ATM **risk identification:** The process of identifying risks using techniques such as brainstorming,  
ATA checklists and failure history. ATT

**risk impact:** The damage that will be caused if the risk become an actual outcome or event.

ATM **risk level:** The importance of a risk as defined by its characteristics impact and likelihood.  
ATA The level of risk can be used to determine the intensity of testing to be performed. A risk  
ATT level can be expressed either qualitatively (e.g. high, medium, low) or quantitatively.

**risk likelihood:** The estimated probability that a risk will become an actual outcome or event.

ATM **risk management:** Systematic application of procedures and practices to the tasks of  
ATA identifying, analyzing, prioritizing, and controlling risk.

ATM **risk mitigation:** See *risk control*.

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**risk type:** A set of risks grouped by one or more common factors such as a quality attribute, cause, location, or potential effect of risk;. A specific set of product risk types is related to the type of testing that can mitigate (control) that risk type. For example the risk of user-interactions being misunderstood can be mitigated by usability testing.

ATT **robustness:** The degree to which a component or system can function correctly in the presence of invalid inputs or stressful environmental conditions. [IEEE 610] See also *error-tolerance, fault-tolerance*.

F **robustness testing:** Testing to determine the robustness of the software product.

ATM **root cause:** A source of a defect such that if it is removed, the occurrence of the defect type is decreased or removed. [CMMI]

ATA **root cause analysis:** An analysis technique aimed at identifying the root causes of defects. By directing corrective measures at root causes, it is hoped that the likelihood of defect recurrence will be minimized.

EITP **RUP:** See *Rational Unified Process*.

## S

**safety:** The capability of the software product to achieve acceptable levels of risk of harm to people, business, software, property or the environment in a specified context of use. [ISO 9126]

**safety critical system:** A system whose failure or malfunction may result in death or serious injury to people, or loss or severe damage to equipment, or environmental harm.

**safety testing:** Testing to determine the safety of a software product.

**sanity test:** See *smoke test*.

- scalability:** The capability of the software product to be upgraded to accommodate increased loads. [After Gerrard]
- scalability testing:** Testing to determine the scalability of the software product.
- scenario testing:** See *use case testing*.
- scorecard:** A representation of summarized performance measurements representing progress towards the implementation of long-term goals. A scorecard provides static measurements of performance over or at the end of a defined interval. See also *balanced scorecard, dashboard*.
- F **scribe:** The person who records each defect mentioned and any suggestions for process improvement during a review meeting, on a logging form. The scribe should ensure that the logging form is readable and understandable.
- scripted testing:** Test execution carried out by following a previously documented sequence of tests.
- F **scripting language:** A programming language in which executable test scripts are written, used by a test execution tool (e.g. a capture/playback tool).
- EITP **SCRUM:** An iterative incremental framework for managing projects commonly used with agile software development. See also *agile software development*.
- security:** Attributes of software products that bear on its ability to prevent unauthorized access, whether accidental or deliberate, to programs and data. [ISO 9126] See also *functionality*.
- F **security testing:** Testing to determine the security of the software product. See also  
F-AT *functionality testing*.  
ATT
- security testing tool:** A tool that provides support for testing security characteristics and vulnerabilities.
- F **security tool:** A tool that supports operational security.
- serviceability testing:** See *maintainability testing*.
- session-based test management:** A method for measuring and managing session-based testing, e.g. exploratory testing.
- session-based testing:** An approach to testing in which test activities are planned as uninterrupted sessions of test design and execution, often used in conjunction with exploratory testing.
- ATM **severity:** The degree of impact that a defect has on the development or operation of a component or system. [After IEEE 610]
- ETM **Shewhart chart:** See *control chart*.
- ATT **short-circuiting:** A programming language/interpreter technique for evaluating compound conditions in which a condition on one side of a logical operator may not be evaluated if the condition on the other side is sufficient to determine the final outcome.
- simulation:** The representation of selected behavioral characteristics of one physical or abstract system by another system. [ISO 2382/1]

- simulator:** A device, computer program or system used during testing, which behaves or operates like a given system when provided with a set of controlled inputs. [After IEEE 610, DO178b] See also *emulator*.
- site acceptance testing:** Acceptance testing by users/customers at their site, to determine whether or not a component or system satisfies the user/customer needs and fits within the business processes, normally including hardware as well as software.
- ETM **S.M.A.R.T. goal methodology:** A methodology whereby objectives are defined very specifically rather than generically. SMART is an acronym derived from the attributes of the objective to be defined: Specific, Measurable, Attainable, Relevant and Timely.
- smoke test:** A subset of all defined/planned test cases that cover the main functionality of a component or system, to ascertaining that the most crucial functions of a program work, but not bothering with finer details. See also *build, verification test, intake test*.
- software:** Computer programs, procedures, and possibly associated documentation and data pertaining to the operation of a computer system. [IEEE 610]
- software attack:** See *attack*.
- Software Failure Mode and Effect Analysis (SFMEA):** See *Failure Mode and Effect Analysis (FMEA)*.
- Software Failure Mode, Effects, and Criticality Analysis (SFMECA):** See *Failure Mode, Effects, and Criticality Analysis (FMECA)*.
- Software Fault Tree Analysis (SFTA):** See *Fault Tree Analysis (FTA)*.
- software feature:** See *feature*.
- software integrity level:** The degree to which software complies or must comply with a set of stakeholder-selected software and/or software-based system characteristics (e.g., software complexity, risk assessment, safety level, security level, desired performance, reliability, or cost) which are defined to reflect the importance of the software to its stakeholders.
- F-AT **software lifecycle:** The period of time that begins when a software product is conceived and  
EITP ends when the software is no longer available for use. The software lifecycle typically includes a concept phase, requirements phase, design phase, implementation phase, test phase, installation and checkout phase, operation and maintenance phase, and sometimes, retirement phase. Note these phases may overlap or be performed iteratively.
- EITP **Software Process Improvement:** A program of activities designed to improve the performance and maturity of the organization's software processes and the results of such a program. [After CMMI]
- software product characteristic:** See *quality attribute*.
- software quality:** The totality of functionality and features of a software product that bear on its ability to satisfy stated or implied needs. [After ISO 9126] See also *quality*.
- software quality characteristic:** See *quality attribute*.
- software test incident:** See *incident*.
- software test incident report:** See *incident report*.
- ATA **Software Usability Measurement Inventory (SUMI):** A questionnaire-based usability test technique for measuring software quality from the end user's point of view. [Veenendaal04]



- source statement:** See *statement*.
- specification:** A document that specifies, ideally in a complete, precise and verifiable manner, the requirements, design, behavior, or other characteristics of a component or system, and, often, the procedures for determining whether these provisions have been satisfied. [After IEEE 610]
- ATA **specification-based technique:** See *black box test design technique*.
- specification-based testing:** See *black box testing*.
- specification-based test design technique:** See *black box test design technique*.
- specified input:** An input for which the specification predicts a result.
- EITP **SPI:** See *Software Process Improvement*.
- ATT **stability:** The capability of the software product to avoid unexpected effects from modifications in the software. [ISO 9126] See also *maintainability*.
- EITP **staged representation:** A model structure wherein attaining the goals of a set of process areas establishes a maturity level; each level builds a foundation for subsequent levels. [CMMI]
- EITP **standard:** Formal, possibly mandatory, set of requirements developed and used to prescribe consistent approaches to the way of working or to provide guidelines (e.g., ISO/IEC standards, IEEE standards, and organizational standards). [After CMMI]
- ETM **standard-compliant testing:** Testing that complies to a set of requirements defined by a standard, e.g., an industry testing standard or a standard for testing safety-critical systems. See also *process-compliant testing*.
- standard software:** See *off-the-shelf software*.
- standards testing:** See *compliance testing*.
- state diagram:** A diagram that depicts the states that a component or system can assume, and shows the events or circumstances that cause and/or result from a change from one state to another. [IEEE 610]
- state table:** A grid showing the resulting transitions for each state combined with each possible event, showing both valid and invalid transitions.
- state transition:** A transition between two states of a component or system.
- F **state transition testing:** A black box test design technique in which test cases are designed to  
ATA execute valid and invalid state transitions. See also *N-switch testing*.
- statement:** An entity in a programming language, which is typically the smallest indivisible unit of execution.
- F **statement coverage:** The percentage of executable statements that have been exercised by a test suite.
- ATT **statement testing:** A white box test design technique in which test cases are designed to execute statements.
- F **static analysis:** Analysis of software development artifacts, e.g. requirements or code, carried  
ATT out without execution of these software development artifacts. Static analysis is usually carried out by means of a supporting tool.
- F **static analysis tool:** See *static analyzer*.
- ATT **static analyzer:** A tool that carries out static analysis.

- static code analysis:** Analysis of source code carried out without execution of that software.
- static code analyzer:** A tool that carries out static code analysis. The tool checks source code, for certain properties such as conformance to coding standards, quality metrics or data flow anomalies.
- F **static testing:** Testing of a software development artifact, e.g., requirements, design or code, without execution of these artifacts, e.g., reviews or static analysis.
- statistical testing:** A test design technique in which a model of the statistical distribution of the input is used to construct representative test cases. See also *operational profile testing*.
- status accounting:** An element of configuration management, consisting of the recording and reporting of information needed to manage a configuration effectively. This information includes a listing of the approved configuration identification, the status of proposed changes to the configuration, and the implementation status of the approved changes. [IEEE 610]
- ATM **STEP:** See *Systematic Test and Evaluation Process*.  
EITP
- storage:** See *resource utilization*.
- storage testing:** See *resource utilization testing*.
- F **stress testing:** A type of performance testing conducted to evaluate a system or component at or beyond the limits of its anticipated or specified workloads, or with reduced availability of resources such as access to memory or servers. [After IEEE 610] See also *performance testing, load testing*.
- F **stress testing tool:** A tool that supports stress testing.
- structural coverage:** Coverage measures based on the internal structure of a component or system.
- structural test design technique:** See *white-box test design technique*.
- F **structural testing:** See *white-box testing*.
- structure-based test design technique:** See *white-box test design technique*.
- ATT **structure-based technique:** See *white-box test design technique*.
- F **structure-based testing:** See *white-box testing*.
- structured walkthrough:** See *walkthrough*.
- F **stub:** A skeletal or special-purpose implementation of a software component, used to develop or test a component that calls or is otherwise dependent on it. It replaces a called component. [After IEEE 610]
- subpath:** A sequence of executable statements within a component.
- suitability:** The capability of the software product to provide an appropriate set of functions for specified tasks and user objectives. [ISO 9126] See also *functionality*.
- ATA **suitability testing:** The process of testing to determine the suitability of a software product
- ATA **SUMI:** See *Software Usability Measurement Inventory*.
- suspension criteria:** The criteria used to (temporarily) stop all or a portion of the testing activities on the test items. [After IEEE 829]

**syntax testing:** A black box test design technique in which test cases are designed based upon the definition of the input domain and/or output domain.

**system:** A collection of components organized to accomplish a specific function or set of functions. [IEEE 610]

**system integration testing:** Testing the integration of systems and packages; testing interfaces to external organizations (e.g. Electronic Data Interchange, Internet).

**system of systems:** Multiple heterogeneous, distributed systems that are embedded in networks at multiple levels and in multiple interconnected domains, addressing large-scale inter-disciplinary common problems and purposes, usually without a common management structure.

**system under test:** See *test object*.

F **system testing:** The process of testing an integrated system to verify that it meets specified requirements. [Hetzel]

ATM **Systematic Test and Evaluation Process:** A structured testing methodology, also used as a  
EITP content-based model for improving the testing process. Systematic Test and Evaluation Process (STEP) does not require that improvements occur in a specific order. See also *content-based model*.

## T

**TDD:** See test-driven development.

F **technical review:** A peer group discussion activity that focuses on achieving consensus on  
ATM the technical approach to be taken. [Gilb and Graham], [IEEE 1028] See also *peer review*.

**test:** A set of one or more test cases. [IEEE 829]

**test analysis:** The process of analyzing the test basis and defining test objectives.

F **test approach:** The implementation of the test strategy for a specific project. It typically  
F-AT includes the decisions made that follow based on the (test) project's goal and the risk  
ATM assessment carried out, starting points regarding the test process, the test design techniques to be applied, exit criteria and test types to be performed

ETM **test architect :** (1) A person who provides guidance and strategic direction for a test organization and for its relationship with other disciplines.  
(2) A person who defines the way testing is structured for a given system, including topics such as test tools and test data management.

F-AT **test automation:** The use of software to perform or support test activities, e.g. test management, test design, test execution and results checking.

F **test basis:** All documents from which the requirements of a component or system can be  
F-AT inferred. The documentation on which the test cases are based. If a document can be amended only by way of formal amendment procedure, then the test basis is called a frozen test basis. [After TMap]

**test bed:** See *test environment*.

F **test case:** A set of input values, execution preconditions, expected results and execution  
ATM postconditions, developed for a particular objective or test condition, such as to exercise a particular program path or to verify compliance with a specific requirement. [After IEEE 610]

- test case design technique:** See *test design technique*.
- F **test case specification:** A document specifying a set of test cases (objective, inputs, test actions, expected results, and execution preconditions) for a test item. [After IEEE 829] See also *test specification*.
- test case suite:** See *test suite*.
- F-AT **test charter:** A statement of test objectives, and possibly test ideas about how to test. Test  
ATA charters are used in exploratory testing. See also *exploratory testing*.
- ATM **test closure:** During the test closure phase of a test process data is collected from completed activities to consolidate experience, testware, facts and numbers. The test closure phase consists of finalizing and archiving the testware and evaluating the test process, including preparation of a test evaluation report. See also *test process*.
- F **test comparator:** A test tool to perform automated test comparison of actual results with expected results.
- test comparison:** The process of identifying differences between the actual results produced by the component or system under test and the expected results for a test. Test comparison can be performed during test execution (dynamic comparison) or after test execution.
- test completion criteria:** See *exit criteria*.
- F **test condition:** An item or event of a component or system that could be verified by one or  
ATM more test cases, e.g. a function, transaction, feature, quality attribute, or structural element.
- F **test control:** A test management task that deals with developing and applying a set of  
ATM corrective actions to get a test project on track when monitoring shows a deviation from  
ATA what was planned. See also *test management*.
- F **test coverage:** See *coverage*.
- test cycle:** Execution of the test process against a single identifiable release of the test object.
- F **test data:** Data that exists (for example, in a database) before a test is executed, and that affects or is affected by the component or system under test.
- test data management:** The process of analyzing test data requirements, designing test data structures, creating and maintaining test data.
- F **test data preparation tool:** A type of test tool that enables data to be selected from existing  
ATA databases or created, generated, manipulated and edited for use in testing.
- test deliverable:** Any test (work) product that must be delivered to someone other than the test (work) product's author. See also *deliverable*.
- F **test design:** (1) See *test design specification*.  
ATM (2) The process of transforming general test objectives into tangible test conditions and  
ATA test cases.
- test design specification:** A document specifying the test conditions (coverage items) for a test item, the detailed test approach and identifying the associated high level test cases. [After IEEE 829] See also *test specification*.
- F **test design technique:** Procedure used to derive and/or select test cases.
- F **test design tool:** A tool that supports the test design activity by generating test inputs from a  
ATA specification that may be held in a CASE tool repository, e.g. requirements management tool, from specified test conditions held in the tool itself, or from code.

- ATM **test director:** A senior manager who manages test managers. *See also* test manager.
- F **test-driven development:** A way of developing software where the test cases are  
 F-AT developed, and often automated, before the software is developed to run those test cases.  
 ETM
- test driver:** *See driver.*
- F **test environment:** An environment containing hardware, instrumentation, simulators, software tools, and other support elements needed to conduct a test. [After IEEE 610]
- F-AT **test estimation:** The calculated approximation of a result related to various aspects of testing  
 ATM (e.g. effort spent, completion date, costs involved, number of test cases, etc.) which is usable even if input data may be incomplete, uncertain, or noisy.
- test evaluation report:** A document produced at the end of the test process summarizing all testing activities and results. It also contains an evaluation of the test process and lessons learned.
- F **test execution:** The process of running a test on the component or system under test,  
 ATM producing actual result(s).  
 ATA
- F-AT **test execution automation:** The use of software, e.g. capture/playback tools, to control the execution of tests, the comparison of actual results to expected results, the setting up of test preconditions, and other test control and reporting functions.
- test execution phase:** The period of time in a software development lifecycle during which the components of a software product are executed, and the software product is evaluated to determine whether or not requirements have been satisfied. [IEEE 610]
- F **test execution schedule:** A scheme for the execution of test procedures. Note: The test procedures are included in the test execution schedule in their context and in the order in which they are to be executed.
- test execution technique:** The method used to perform the actual test execution, either manual or automated.
- F **test execution tool:** A type of test tool that is able to execute other software using an  
 ATA automated test script, e.g. capture/playback. [Fewster and Graham]  
 ATT
- test fail:** *See fail.*
- test generator:** *See test data preparation tool.*
- F **test harness:** A test environment comprised of stubs and drivers needed to execute a test.
- ATM **test implementation:** The process of developing and prioritizing test procedures, creating test  
 ATA data and, optionally, preparing test harnesses and writing automated test scripts.
- EITP **test improvement plan:** A plan for achieving organizational test process improvement objectives based on a thorough understanding of the current strengths and weaknesses of the organization's test processes and test process assets. [After CMMI]
- test incident:** *See incident.*
- test incident report:** *See incident report.*
- test infrastructure:** The organizational artifacts needed to perform testing, consisting of test environments, test tools, office environment and procedures.

- test input:** The data received from an external source by the test object during test execution. The external source can be hardware, software or human.
- test item:** The individual element to be tested. There usually is one test object and many test items. See also *test object*.
- test item transmittal report:** See *release note*.
- F **test leader:** See *test manager*.
- F **test level:** A group of test activities that are organized and managed together. A test level is  
ATM linked to the responsibilities in a project. Examples of test levels are component test, integration test, system test and acceptance test. [After TMap]
- F **test log:** A chronological record of relevant details about the execution of tests. [IEEE 829]  
ATM
- test logging:** The process of recording information about tests executed into a test log.
- ATM **test management:** The planning, estimating, monitoring and control of test activities, typically carried out by a test manager.
- F **test management tool:** A tool that provides support to the test management and control part  
ATT of a test process. It often has several capabilities, such as testware management, scheduling of tests, the logging of results, progress tracking, incident management and test reporting.
- F **test manager:** The person responsible for project management of testing activities and resources, and evaluation of a test object. The individual who directs, controls, administers, plans and regulates the evaluation of a test object.
- ATM **Test Maturity Model integration:** A five level staged framework for test process  
EITP improvement, related to the Capability Maturity Model Integration (CMMI), that describes the key elements of an effective test process.
- ETM **test mission:** The purpose of testing for an organization, often documented as part of the test policy. See also *test policy*.
- F **test monitoring:** A test management task that deals with the activities related to periodically  
ATM checking the status of a test project. Reports are prepared that compare the actuals to that  
ATA which was planned. See also *test management*.
- test object:** The component or system to be tested. See also *test item*.
- F **test objective:** A reason or purpose for designing and executing a test.
- F-AT **test oracle:** A source to determine expected results to compare with the actual result of the software under test. An oracle may be the existing system (for a benchmark), other software, a user manual, or an individual's specialized knowledge, but should not be the code. [After Adrion]
- test outcome:** See *result*.
- test pass:** See *pass*.
- test performance indicator:** A high level metric of effectiveness and/or efficiency used to guide and control progressive test development, e.g. Defect Detection Percentage (DDP).
- test phase:** A distinct set of test activities collected into a manageable phase of a project, e.g. the execution activities of a test level. [After Gerrard]
- F **test plan:** A document describing the scope, approach, resources and schedule of intended

ATM test activities. It identifies amongst others test items, the features to be tested, the testing tasks, who will do each task, degree of tester independence, the test environment, the test design techniques and entry and exit criteria to be used, and the rationale for their choice, and any risks requiring contingency planning. It is a record of the test planning process. [After IEEE 829]

ATM **test planning:** The activity of establishing or updating a test plan.  
ATA

**Test Point Analysis (TPA):** A formula based test estimation method based on function point analysis. [TMap]

F **test policy:** A high level document describing the principles, approach and major objectives  
ATM of the organization regarding testing.  
EITP  
ETM

F **test procedure:** See *test procedure specification*.  
ATM

F **test procedure specification:** A document specifying a sequence of actions for the execution  
ATM of a test. Also known as test script or manual test script. [After IEEE 829] See also *test specification*.

**test process:** The fundamental test process comprises test planning and control, test analysis and design, test implementation and execution, evaluating exit criteria and reporting, and test closure activities.

EITP **Test Process Group:** A collection of (test) specialists who facilitate the definition, maintenance, and improvement of the test processes used by an organization. [After CMMI]

EITP **test process improvement manifesto:** A statement that echoes the agile manifesto, and defines values for improving the testing process. The values are:

- flexibility over detailed processes
- best practices over templates
- deployment orientation over process orientation
- peer reviews over quality assurance (departments)
- business driven over model driven. [Veenendaal08]

EITP **test process improver:** A person implementing improvements in the test process based on a test improvement plan.

**test progress report:** A document summarizing testing activities and results, produced at regular intervals, to report progress of testing activities against a baseline (such as the original test plan) and to communicate risks and alternatives requiring a decision to management.

**test record:** See *test log*.

**test recording:** See *test logging*.

**test report:** See *test summary report* and *test progress report*.

**test reporting:** Collecting and analyzing data from testing activities and subsequently consolidating the data in a report to inform stakeholders. See also *test process*.

**test reproducibility:** An attribute of a test indicating whether the same results are produced each time the test is executed.

**test requirement:** See *test condition*.

**test result:** See *result*.

**test rig:** See *test environment*.

**test run:** Execution of a test on a specific version of the test object.

**test run log:** See *test log*.

**test scenario:** See *test procedure specification*.

**test schedule:** A list of activities, tasks or events of the test process, identifying their intended start and finish dates and/or times, and interdependencies.

F **test script:** Commonly used to refer to a test procedure specification, especially an automated  
ATM one.

**test session:** An uninterrupted period of time spent in executing tests. In exploratory testing, each test session is focused on a charter, but testers can also explore new opportunities or issues during a session. The tester creates and executes on the fly and records their progress. See also *exploratory testing*.

**test set:** See *test suite*.

**test situation:** See *test condition*.

**test specification:** A document that consists of a test design specification, test case specification and/or test procedure specification.

**test specification technique:** See *test design technique*.

**test stage:** See *test level*.

F **test strategy:** A high-level description of the test levels to be performed and the testing within  
F-AT those levels for an organization or programme (one or more projects).

ATM

ATA

ETM

F **test suite:** A set of several test cases for a component or system under test, where the post  
condition of one test is often used as the precondition for the next one.

F **test summary report:** A document summarizing testing activities and results. It also contains  
ATM an evaluation of the corresponding test items against exit criteria. [After IEEE 829]

**test target:** A set of exit criteria.

**test technique:** See *test design technique*.

EITP **test tool:** A software product that supports one or more test activities, such as planning and  
control, specification, building initial files and data, test execution and test analysis.  
[TMap] See also *CAST*.

**test type:** A group of test activities aimed at testing a component or system focused on a specific test objective, i.e. functional test, usability test, regression test etc. A test type may take place on one or more test levels or test phases. [After TMap]

ATT **testability:** The capability of the software product to enable modified software to be tested.  
[ISO 9126] See also *maintainability*.

**testability review:** A detailed check of the test basis to determine whether the test basis is at an adequate quality level to act as an input document for the test process. [After TMap]



**testable requirement:** A requirements that is stated in terms that permit establishment of test designs (and subsequently test cases) and execution of tests to determine whether the requirement has been met. [After IEEE 610]

F **tester:** A skilled professional who is involved in the testing of a component or system.

F **testing:** The process consisting of all lifecycle activities, both static and dynamic, concerned with planning, preparation and evaluation of software products and related work products to determine that they satisfy specified requirements, to demonstrate that they are fit for purpose and to detect defects.

F **testware:** Artifacts produced during the test process required to plan, design, and execute tests, such as documentation, scripts, inputs, expected results, set-up and clear-up procedures, files, databases, environment, and any additional software or utilities used in testing. [After Fewster and Graham]

**thread testing:** An approach to component integration testing where the progressive integration of components follows the implementation of subsets of the requirements, as opposed to the integration of components by levels of a hierarchy.

**three point estimation:** A test estimation method using estimated values for the “best case”, “worst case”, and “most likely case” of the matter being estimated, to define the degree of certainty associated with the resultant estimate.

**time behavior:** See *performance*.

ATM **TMMi:** See *Test Maturity Model integration*.

EITP

**top-down testing:** An incremental approach to integration testing where the component at the top of the component hierarchy is tested first, with lower level components being simulated by stubs. Tested components are then used to test lower level components. The process is repeated until the lowest level components have been tested. See also *integration testing*.

EITP **Total Quality Management:** An organization-wide management approach centered on quality, based on the participation of all members of the organization and aiming at long-term success through customer satisfaction, and benefits to all members of the organization and to society. Total Quality Management consists of planning, organizing, directing, control, and assurance. [After ISO 8402]

EITP **TPG:** See *Test Process Group*.

ATM **TPI Next:** A continuous business-driven framework for test process improvement that  
EITP describes the key elements of an effective and efficient test process.

EITP **TQM:** See *Total Quality Management*.

F **traceability:** The ability to identify related items in documentation and software, such as requirements with associated tests. See also *horizontal traceability*, *vertical traceability*.

**traceability matrix:** A two-dimensional table, which correlates two entities (e.g., requirements and test cases). The table allows tracing back and forth the links of one entity to the other, thus enabling the determination of coverage achieved and the assessment of impact of proposed changes.

EITP **transactional analysis:** The analysis of transactions between people and within people’s minds; a transaction is defined as a stimulus plus a response. Transactions take place between people and between the ego states (personality segments) within one person’s mind.

EITP **transcendent-based quality:** A view of quality, wherein quality cannot be precisely defined, but we know it when we see it, or are aware of its absence when it is missing. Quality depends on the perception and affective feelings of an individual or group of individuals towards a product. [After Garvin] See also *manufacturing-based quality, product-based quality, user-based quality, value-based quality*.

## U

ATA **understandability:** The capability of the software product to enable the user to understand whether the software is suitable, and how it can be used for particular tasks and conditions of use. [ISO 9126] See also *usability*.

**unit:** See *component*.

F **unit test framework:** A tool that provides an environment for unit or component testing in  
F-AT which a component can be tested in isolation or with suitable stubs and drivers. It also provides other support for the developer, such as debugging capabilities. [Graham]

**unit testing:** See *component testing*.

**unreachable code:** Code that cannot be reached and therefore is impossible to execute.

**usability:** The capability of the software to be understood, learned, used and attractive to the user when used under specified conditions. [ISO 9126]

F **usability testing:** Testing to determine the extent to which the software product is  
F-AT understood, easy to learn, easy to operate and attractive to the users under specified  
ATA conditions. [After ISO 9126]

**use case:** A sequence of transactions in a dialogue between an actor and a component or system with a tangible result, where an actor can be a user or anything that can exchange information with the system.

F **use case testing:** A black box test design technique in which test cases are designed to  
ATA execute scenarios of use cases.

F **user acceptance testing:** See *acceptance testing*.

EITP **user-based quality:** A view of quality, wherein quality is the capacity to satisfy needs, wants and desires of the user(s). A product or service that does not fulfill user needs is unlikely to find any users. This is a context dependent, contingent approach to quality since different business characteristics require different qualities of a product. [after Garvin] See also *manufacturing-based quality, product-based quality, transcendent-based quality, value-based quality*.

**user scenario testing:** See *use case testing*

F-AT **user story:** A high-level user or business requirement commonly used in agile software development, typically consisting of one or more sentences in the everyday or business language capturing what functionality a user needs, any non-functional criteria, and also includes acceptance criteria. See also *agile software development, requirement*.

ATA **user story testing:** A black box test design technique in which test cases are designed based on user stories to verify their correct implementation. See also *user story*.

**user test:** A test whereby real-life users are involved to evaluate the usability of a component or system.

## V

- F **V-model:** A framework to describe the software development lifecycle activities from requirements specification to maintenance. The V-model illustrates how testing activities can be integrated into each phase of the software development lifecycle.
- F **validation:** Confirmation by examination and through provision of objective evidence that the requirements for a specific intended use or application have been fulfilled. [ISO 9000]
- EITP **value-based quality:** A view of quality, wherein quality is defined by price. A quality product or service is one that provides desired performance at an acceptable cost. Quality is determined by means of a decision process with stakeholders on trade-offs between time, effort and cost aspects. [After Garvin] See also *manufacturing-based quality, product-based quality, transcendent-based quality, user-based quality*.
- variable:** An element of storage in a computer that is accessible by a software program by referring to it by a name.
- F **verification:** Confirmation by examination and through provision of objective evidence that specified requirements have been fulfilled. [ISO 9000]
- F **version control:** See *configuration control*.
- vertical traceability:** The tracing of requirements through the layers of development documentation to components.
- volume testing:** Testing where the system is subjected to large volumes of data. See also *resource-utilization testing*.

## W

- F **walkthrough:** A step-by-step presentation by the author of a document in order to gather information and to establish a common understanding of its content. [Freedman and Weinberg, IEEE 1028] See also *peer review*.
- ATM **WAMMI:** See *Website Analysis and MeasureMent Inventory*.
- WBS:** See *Work Breakdown Structure*.
- ATA **Website Analysis and MeasureMent Inventory (WAMMI):** A questionnaire-based usability test technique for measuring web site software quality from the end user's point of view.
- white-box technique:** See *white-box test design technique*.
- F **white-box test design technique:** Procedure to derive and/or select test cases based on an analysis of the internal structure of a component or system.
- ATT
- F **white-box testing:** Testing based on an analysis of the internal structure of the component or system.
- ATM **Wide Band Delphi:** An expert based test estimation technique that aims at making an accurate estimation using the collective wisdom of the team members.
- ATT **wild pointer:** A pointer that references a location that is out of scope for that pointer or that does not exist. See also *pointer*.
- Work Breakdown Structure:** An arrangement of work elements and their relationship to each other and to the end product. [CMMI]

## Annex A References

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